

	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q
1																	
2		Testprotokoll															
3																	
4		Pexaria nr. 1	WREX2A	SFP													
5																	
6	1																
7	2		->360mA														
8	3																
9	4																
10	5																
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16	11																
17	12																
18	13																
19	14																
20	15																
21	16																
22	17																
23	18		->360mA														
24	19																
25	20																
26	21																
27	22																
28	23																
29	24																
30	25																
31	26		->516mA														
32	27																
33	28		->528mA														
34	29																
35	30																
36	31																
37	32																
38	33																
39	34		<=>374mA														
40	35																
41	36		531mA														
42	37																
43	38			MAC 00:26:7B:00:04:01													
44	39																
45	40																
46	41		->603mA														
47	42																
48	43																
49	44			IP 192.168.128.57	TRACK OK	Offset gelegentlich bis zu +16ps Bereich -10ps/+16ps											
50	45																
51	46																
52	47																
53	48																
54	49																
55	50		->604 mA		LVDs+ +CLK Outputs OK												
56	50.1																
57	50.2																
58	50.3																
59	50.4			IP 192.168.128.57		Offset gelegentlich bis zu -31ps Bereich -31ps/+4ps											
60																	
61																	
62																	
63																	
64																	
65																	

File Vertical Timebase Trigger Display Cursors Measure Math Analysis Utilities Help



C1 DC
500 mV/div
4 mV offset

Timebase -17.2 ns
5.00 ns/div Stop 350 mV
1 kS 20 GS/s Edge Positive

Utilities Status Remote Hardcopy Aux Output Date/Time Options

File Format: JPEG - JFIF Compliant (.jpg) File Name: LeCroy7.jpg

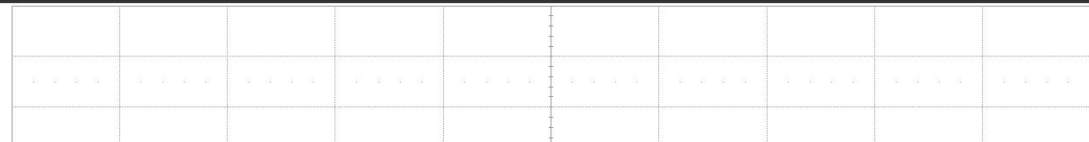
Colors: Print Colors: Print colors use a white background to save tonerlink.

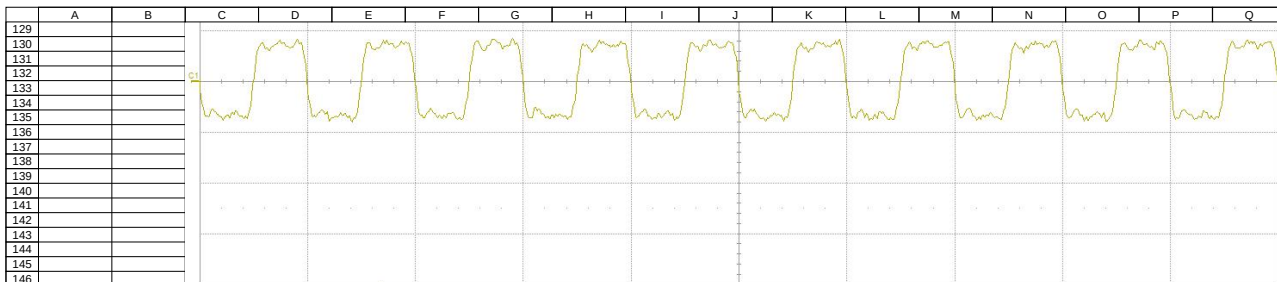
Directory: E:\ Browse

Hardcopy Area DSO Window

Hardcopy saved to: E:\LeCroy6.jpg 9/14/2017 8:59:50 AM

File Vertical Timebase Trigger Display Cursors Measure Math Analysis Utilities Help





500 mV/div
4 mV offset

Timebase: 16.6 ns
Trigger: Stop
500 mV
1 kS
20 GS/s
Edge
Positive

Utilities Status Remote Hardcopy Aux Output Date/Time Options

File Format: JPEG - JFIF Compliant (.jpg) File Name: LeCroy5.jpg

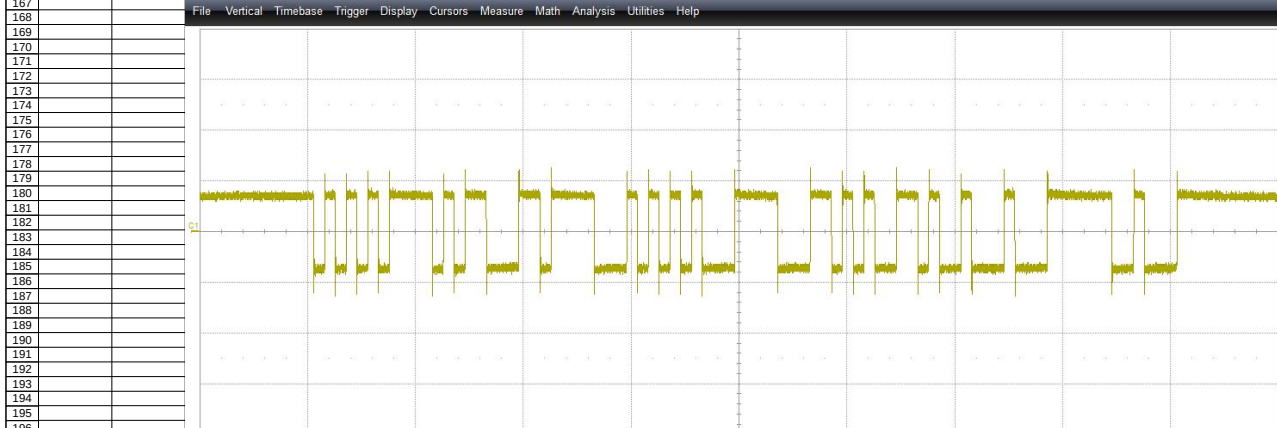
Colors: Print

Print colors use a white background to save tonerink.

Hardcopy Area DSO Window

9/14/2017 6:03:10 AM

Hardcopy saved to: E:\LeCroy4.jpg



500 mV/div
4 mV offset

Timebase: 688 ns
Trigger: Stop
350 mV
40 kS
20 GS/s
Edge
Positive

Utilities Status Remote Hardcopy Aux Output Date/Time Options

File Format: JPEG - JFIF Compliant (.jpg) File Name: LeCroy8.jpg

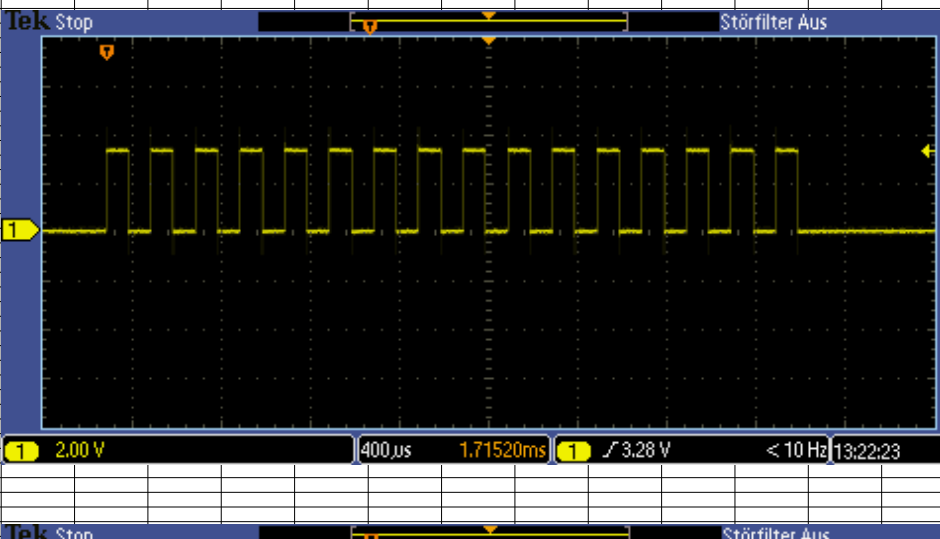
Colors: Print

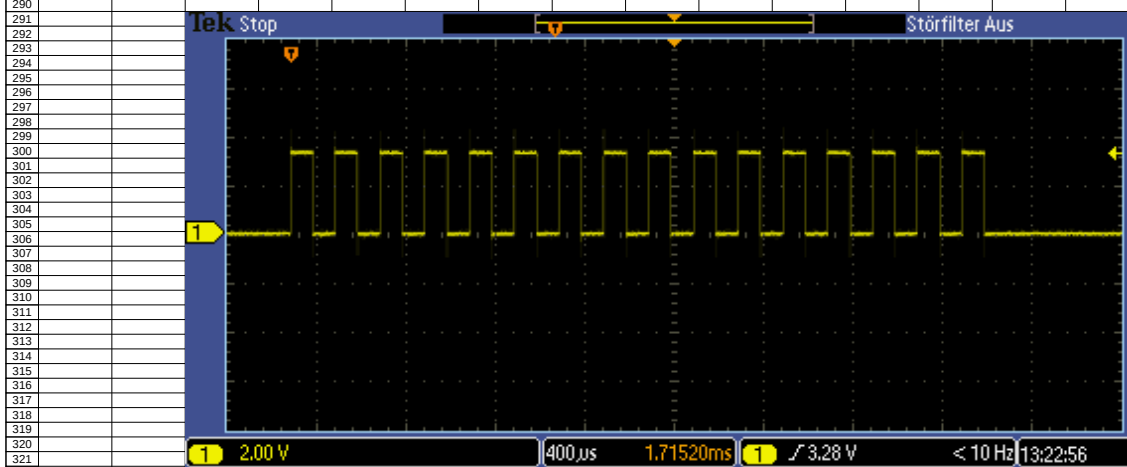
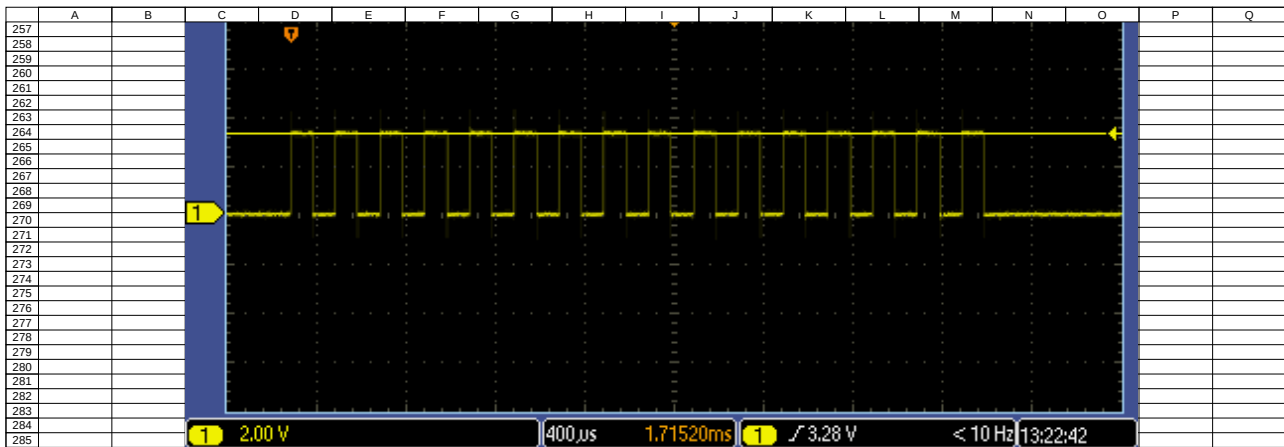
Print colors use a white background to save tonerink.

Hardcopy Area DSO Window

9/14/2017 9:00:29 AM

TELEDYNE LECROY





Gelegentlich gibt es EEPROM not large enough bei der Programmierung der USB-Firmware

volker@belpc125: ~/Projekte/bel_projects/ip

```

Datei Bearbeiten Ansicht Suchen Terminal Hilfe
336 write on-chip, addr 0x0c51 len 27 (0x001b)
337 write on-chip, addr 0x0d58 len 8 (0x0008)
338 write on-chip, addr 0x08b8 len 175 (0x00af)
339 write on-chip, addr 0x0d14 len 16 (0x0010)
340 write on-chip, addr 0x0cc9 len 22 (0x0016)
341 write on-chip, addr 0x0967 len 152 (0x0098)
342 write on-chip, addr 0x0000 len 3 (0x0003)
343 write on-chip, addr 0x07f3 len 12 (0x000c)
344 write on-chip, addr 0x0d03 len 17 (0x0011)
345 ... WROTE: 3359 bytes, 41 segments, avg 81
346 reset CPU
347
348 get EEPROM size, addr 0x0000 len 1 (0x0001)
349 don't see a large enough EEPROM
350
351 volker@belpc125:~/Projekte/bel_projects/ip_cores/etherbone-core/hdl/eb_usb_core$
352 sudo ./flash-fx2lp.sh -E
353 microcontroller type: fx2lp
354 single stage: load on-chip memory
355 open RAM hexfile image erase_eeeprom.ihx
356 stop CPU
357
358 write on-chip, addr 0x0000 len 3 (0x0003)
359 write on-chip, addr 0x005f len 3 (0x0003)
360 write on-chip, addr 0x0003 len 3 (0x0003)
361 write on-chip, addr 0x0062 len 190 (0x00be)
362 write on-chip, addr 0x0035 len 6 (0x0006)
363 write on-chip, addr 0x0013 len 34 (0x0022)
364 write on-chip, addr 0x003b len 36 (0x0024)
365 write on-chip, addr 0x0006 len 13 (0x000d)
366 write on-chip, addr 0x0120 len 4 (0x0004)
367 ... WROTE: 292 bytes, 9 segments, avg 32
368 reset CPU
369
370 volker@belpc125:~/Projekte/bel_projects/ip_cores/etherbone-core/hdl/eb_usb_core$
371 sudo ./flash-fx2lp.sh
372 microcontroller type: fx2lp
373 1st stage: load 2nd stage loader
374 open RAM hexfile image vend_ax.hex
375 stop CPU
376
377 write on-chip, addr 0x0d3e len 10 (0x000a)
378 write on-chip, addr 0x0e4d len 352 (0x0160)
379 write on-chip, addr 0x0d33 len 11 (0x000b)
380 write on-chip, addr 0x039a len 691 (0x02b3)
381 write on-chip, addr 0x0033 len 3 (0x0003)
382 write on-chip, addr 0x0d60 len 4 (0x0004)
383
384

```

	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q
385			write on-chip, addr 0x0c6c len				25	(0x0019)									
386			write on-chip, addr 0x07ff len				1	(0x0001)									
387			write on-chip, addr 0x0d64 len				6	(0x0006)									
388			write on-chip, addr 0x0d48 len				8	(0x0008)									
389			write on-chip, addr 0x0cdf len				18	(0x0012)									
390			write on-chip, addr 0x0d50 len				8	(0x0008)									
391			write on-chip, addr 0x0cf1 len				18	(0x0012)									
392			write on-chip, addr 0x0d6a len				6	(0x0006)									
393			write on-chip, addr 0x0080 len				794	(0x031a)									
394			write on-chip, addr 0x0c32 len				31	(0x001f)									
395			write on-chip, addr 0x0c9d len				44	(0x002c)									
396			write on-chip, addr 0x0b19 len				55	(0x0037)									
397			write on-chip, addr 0x0c85 len				24	(0x0018)									
398			write on-chip, addr 0x0b50 len				55	(0x0037)									
399			write on-chip, addr 0x09ff len				1	(0x0001)									
400			write on-chip, addr 0x0d70 len				35	(0x0023)									
401			write on-chip, addr 0x0a00 len				156	(0x009c)									
402			write on-chip, addr 0x0bb6 len				44	(0x002c)									
403			write on-chip, addr 0x0b87 len				47	(0x002f)									
404			write on-chip, addr 0x07ad len				70	(0x0046)									
405			write on-chip, addr 0x0be2 len				80	(0x0050)									
406			write on-chip, addr 0x0043 len				3	(0x0003)									
407			write on-chip, addr 0x0053 len				3	(0x0003)									
408			write on-chip, addr 0x0800 len				184	(0x00b8)									
409			write on-chip, addr 0x0a9c len				125	(0x007d)									
410			write on-chip, addr 0x0d24 len				15	(0x000f)									
411			write on-chip, addr 0x0c51 len				27	(0x001b)									
412			write on-chip, addr 0x0d58 len				8	(0x0008)									
413			write on-chip, addr 0x08b8 len				175	(0x00af)									
414			write on-chip, addr 0x0d14 len				16	(0x0010)									
415			write on-chip, addr 0x0cc9 len				22	(0x0016)									
416			write on-chip, addr 0x0967 len				152	(0x0098)									
417			write on-chip, addr 0x0000 len				3	(0x0003)									
418																	
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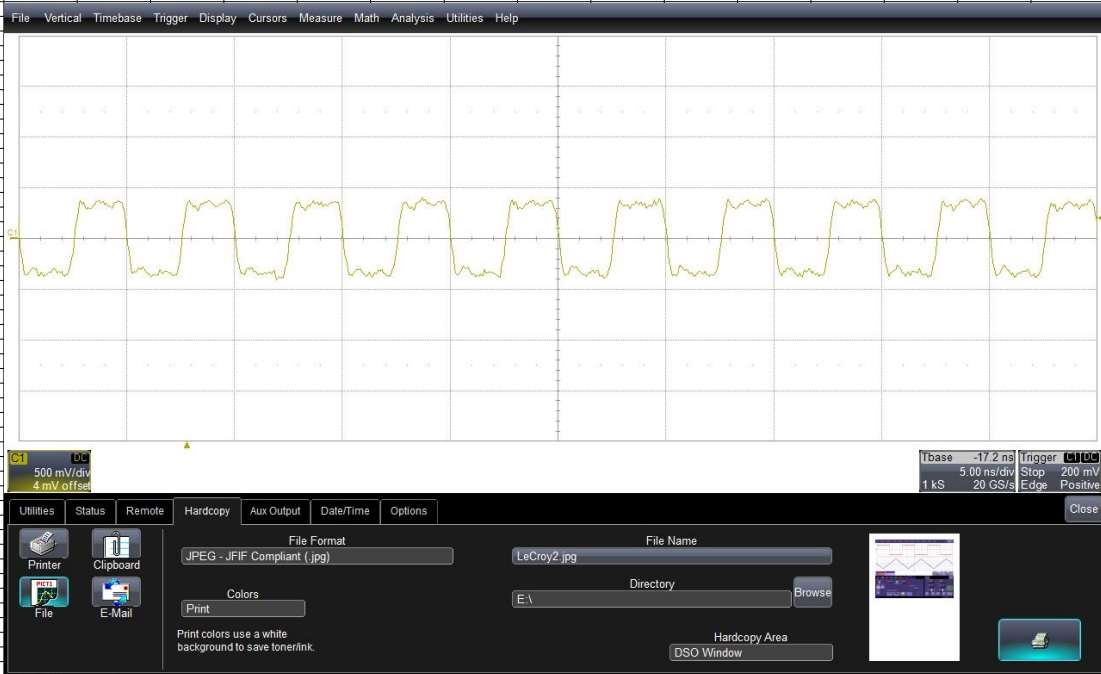
	R	S	T	U	V	W	X	Y	Z	AA	AB	AC	AD	AE	AF
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333	cores/etherbone-core/hdl/eb_usb_core														
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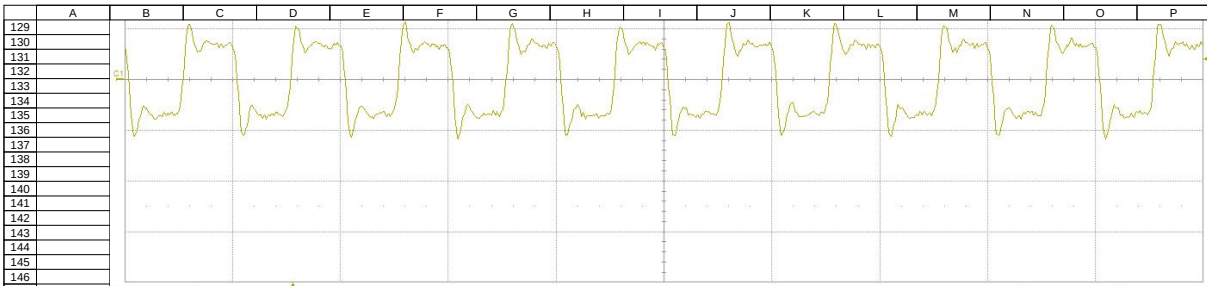
	R	S	T	U	V	W	X	Y	Z	AA	AB	AC	AD	AE	AF
385															
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	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O
1															
2		Testprotokoll													
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4		Pexaria nr. 2	WREX2A	SFP											
5															
6	1														
7	2		>353mA												
8	3														
9	4														
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18	13														
19	14														
20	15														
21	16														
22	17														
23	18		>369mA												
24	19														
25	20														
26	21														
27	22														
28	23														
29	24														
30	25														
31	26		>511mA	Dauert 20sec bis grüne Schrift											
32	27			Don't see large enough EEPROM ... Fehler tritt nach 2. mal nicht mehr Auf (nicht zuverlässiger Init eines Kinfig-Bits?)											
33	28														
34	29														
35	30		>517mA	Don't see large enough EEPROM ... Fehler verschwindet nach 2x ERASE											
36	31														
37	32														
38	33														
39	34		<374mA												
40	35														
41	36		519mA												
42	37														
43	38			MAC 00:26:7B:00:04:02											
44	39														
45	40														
46	41		>591mA												
47	42														
48	43														
49	44	600mA		IP BOOTP RUNNING	TRACK OK	Offset gelegentlich bis zu +20ps Bereich -10ps/+16ps									
50	45														
51	46														
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53	48														
54	49														
55	50					LVDs+ +CLK Outputs OK									
56	50.1														
57	50.2														
58	50.3														
59	50.4			IP 192.168.128.57		Offset gelegentlich bis zu +16ps , Bereich -19ps/+16ps									
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66		200MHz LVDs Stiffliste													
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	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P
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2		Testprotokoll														
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4		Pexaria nr. 3	WREX2A	SFP	andere SW - Stromverbrauch											
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7	2		>320mA													
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23	18		>360mA													
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29	24															
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31	26		>513mA													
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37	32															
38	33															
39	34		<372mA													
40	35															
41	36		520mA													
42	37															
43	38			MAC 00:26:7B:00:04:03												
44	39															
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46	41															
47	42															
48	43															
49	44			IP 192.168.128.57	TRACK OK	Offset gelegentlich bis zu +17ps Bereich -10ps/+17ps										BOOTP
50	45															
51	46															
52	47															
53	48															
54	49															
55	50		>593 mA			LVDs+ +CLK Outputs OK										
56	50.1															
57	50.2															
58	50.3															
59	50.4			IP 192.168.128.57												
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500 mV/div
4 mV offset

1kS 5.00 ns/div Norm. 200 mV
20 GS/s Edge Positive

Trigger C1 DC

Utilities Status Remote Hardcopy Aux Output Date/Time Options

File Format: JPEG - JFIF Compliant (.jpg) File Name: LeCroy.jpg

Directory: E:\ Browse

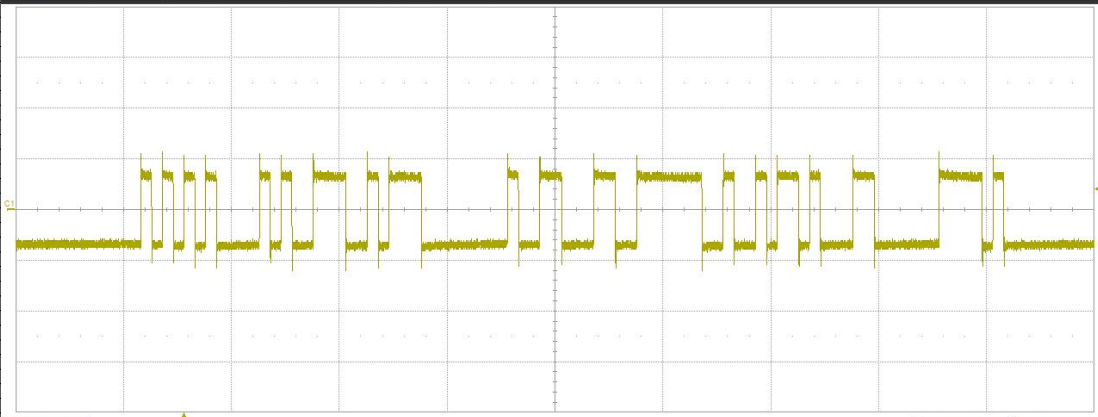
Print Colors

Print colors use a white background to save tonerfink.

Hardcopy Area: DSO Window

TELEDYNE LECROY 9/19/2017 7:57:16 AM

File Vertical Timebase Trigger Display Cursors Measure Math Analysis Utilities Help



500 mV/div
4 mV offset

40 kS 200 ns/div Stop 200 mV
20 GS/s Edge Positive

Trigger C1 DC

Utilities Status Remote Hardcopy Aux Output Date/Time Options

File Format: JPEG - JFIF Compliant (.jpg) File Name: LeCroy1.jpg

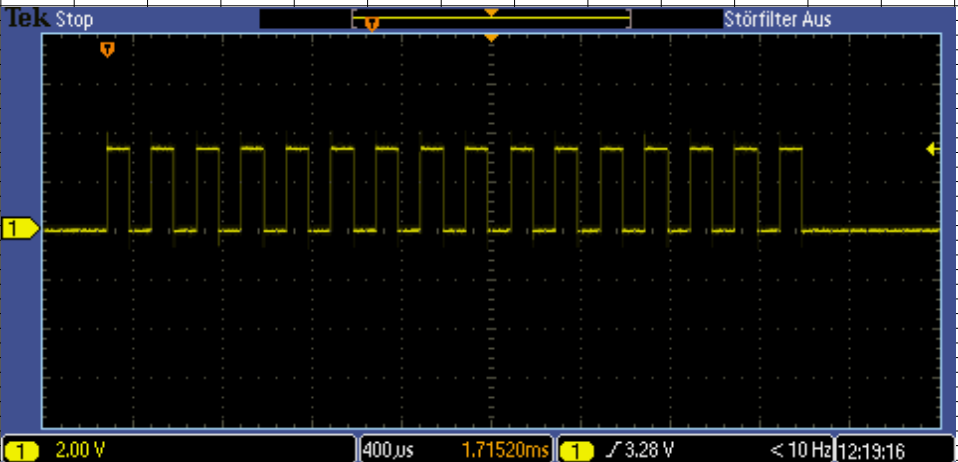
Directory: E:\ Browse

Print Colors

Print colors use a white background to save tonerfink.

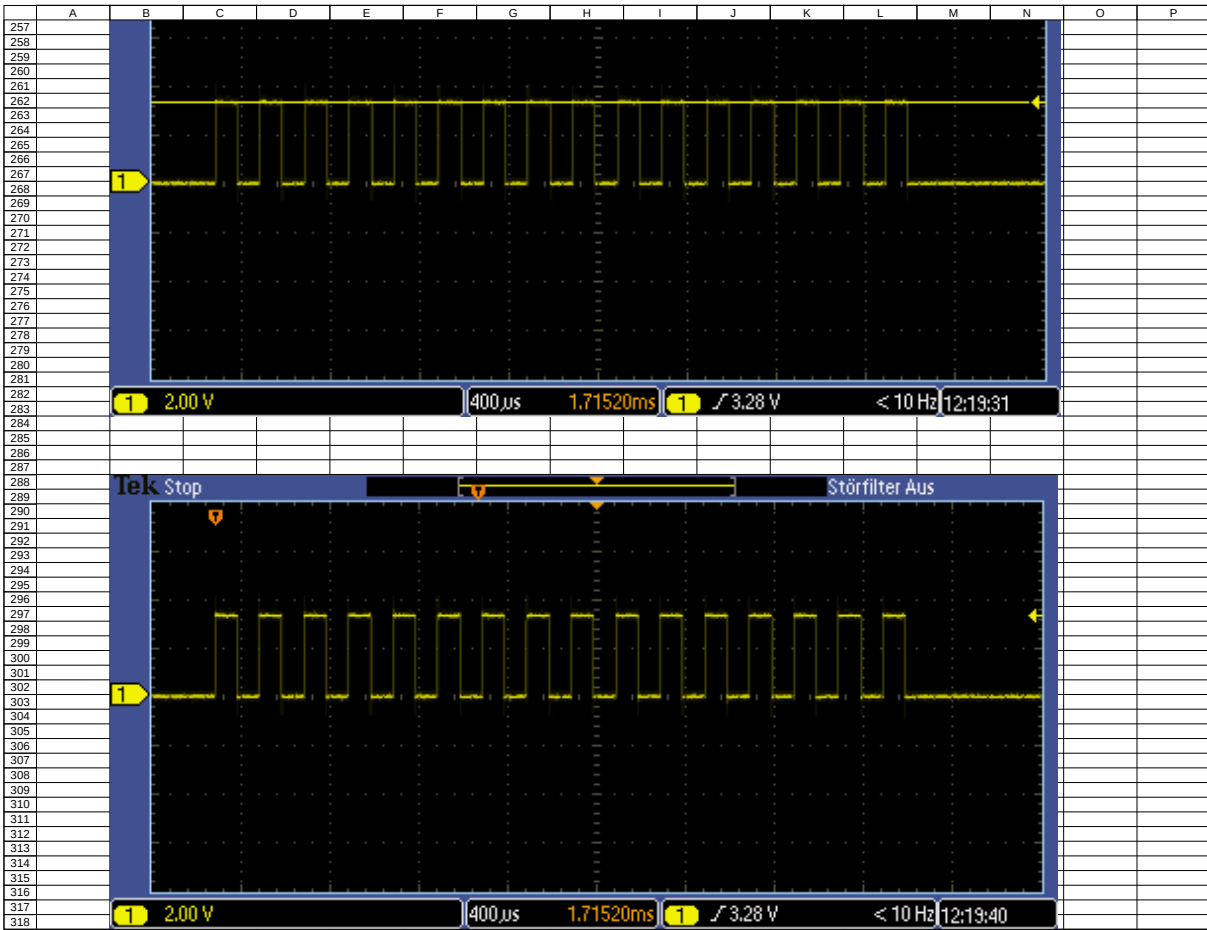
Hardcopy Area: DSO Window

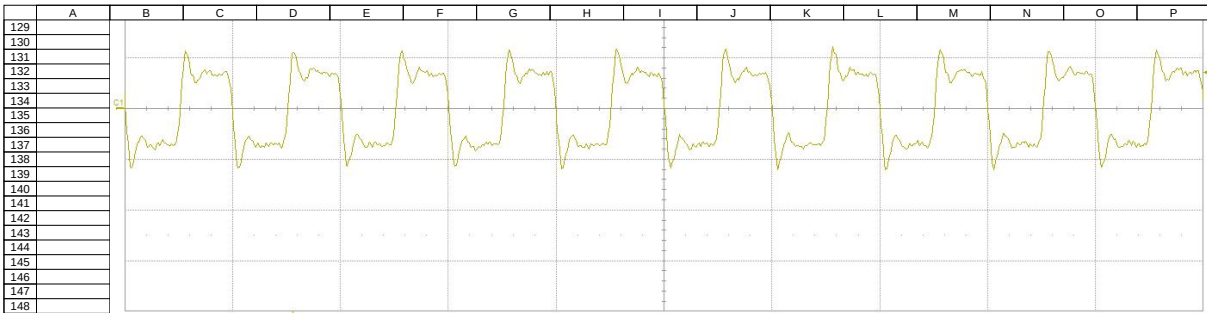
TELEDYNE LECROY 9/19/2017 7:57:43 AM



2.00 V 400 μs 1.71520ms 3.28 V < 10 Hz 12:19:16







500 mV/div
4 mV offset

1K 5.00 ns/div 20 GS/s

Trigger 1 DC Stop 350 mV Edge Positive

Utilities Status Remote Hardcopy Aux Output Date/Time Options

File Format: JPEG - JFIF Compliant (.jpg) File Name: LeCroy1.jpg

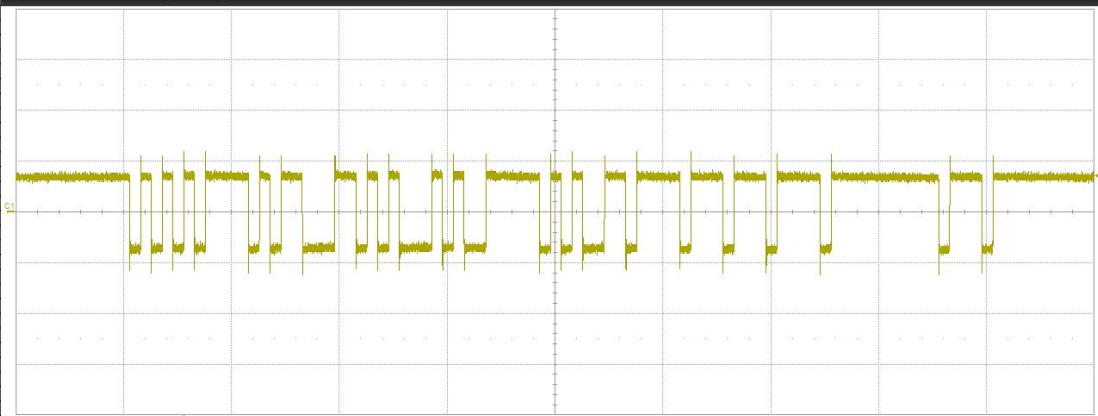
Colors: Print

Print colors use a white background to save tonerink.

Hardcopy Area: DSO Window

TELEDYNE LECROY 9/18/2017 9:09:06 AM

File Vertical Timebase Trigger Display Cursors Measure Math Analysis Utilities Help



500 mV/div
4 mV offset

40 kS 200 ns/div 20 GS/s

Trigger 1 DC Stop 350 mV Edge Positive

Utilities Status Remote Hardcopy Aux Output Date/Time Options

File Format: JPEG - JFIF Compliant (.jpg) File Name: LeCroy2.jpg

Colors: Print

Print colors use a white background to save tonerink.

Hardcopy Area: DSO Window

TELEDYNE LECROY 9/18/2017 9:09:58 AM

