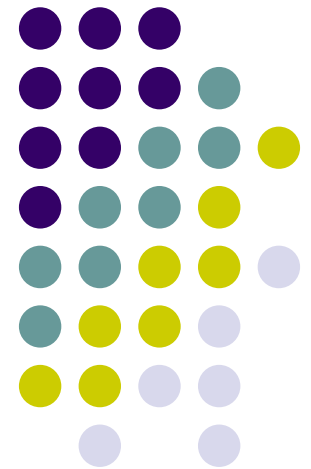
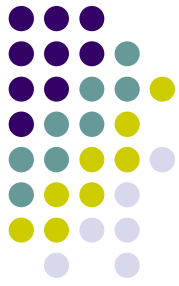


# ASIC Development for APD readout of the PANDA EMC



# Overview

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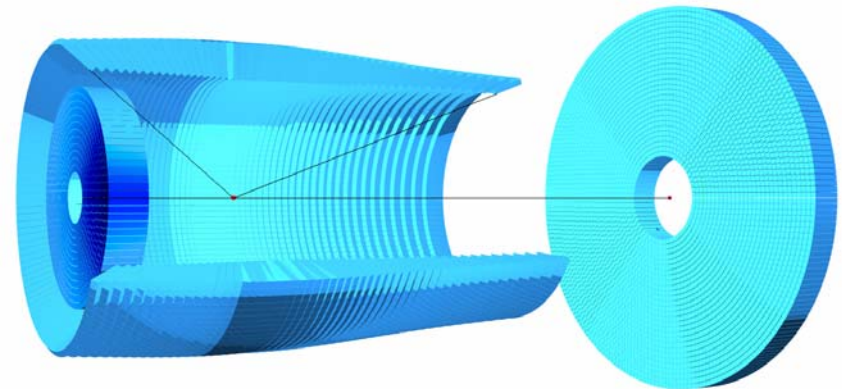
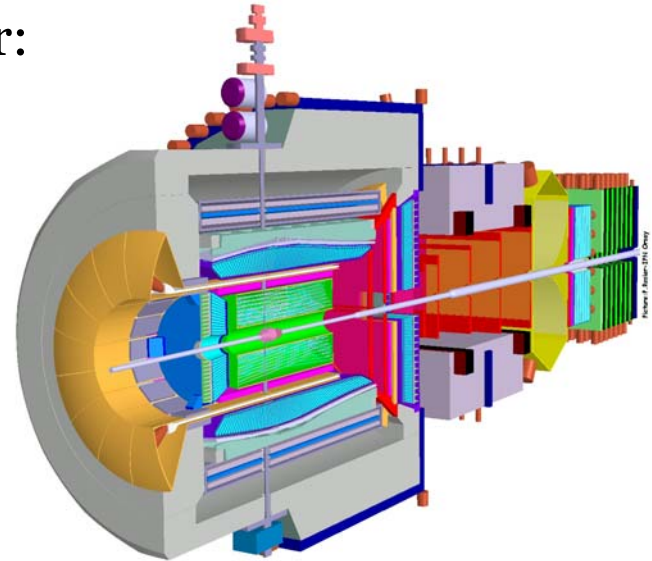


- **Avalanche Photo Diode** - Readout
- First calculations
- Prototype requirements
  
- Summary

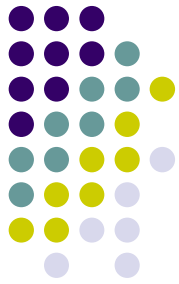
# PANDA Detector



- Layout of the electromagnetic calorimeter:
- 22000 crystals
- Three detector components:
  - Barrel
  - One front and rear endcaps
- Almost  $4\pi$  solid angle coverage
- Evaluation of two scintillator-types:
  - $\text{Bi}_4\text{Ge}_3\text{O}_{12}$  (BGO) and  $\text{PbWO}_4$  (PWO)
- Envisaged energy resolution:  $< 1\%$  at 1 GeV
- Calorimeter in magnetic field



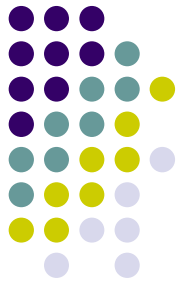
# PWO vs. BGO



Property	PbWO <sub>4</sub> PWO	BGO
Density [g/cm <sup>3</sup> ]	8.28	7.13
Rad. length [cm]	0.89	1.12
Moliere rad [cm]	2.19	2.33
dE/dx [MeV/cm]	13.0	9.2
Decay time [ns]	5 - 15	60 - 300
Max. emission [nm]	420 - 440	480
Rel. Lightyield (NaI(Tl))	0.01	0.15

# Szintillator

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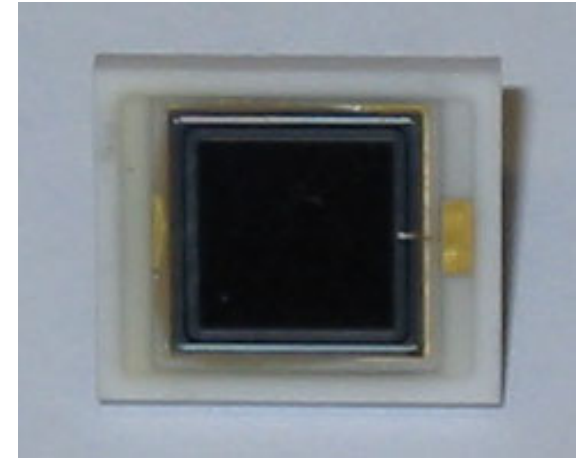
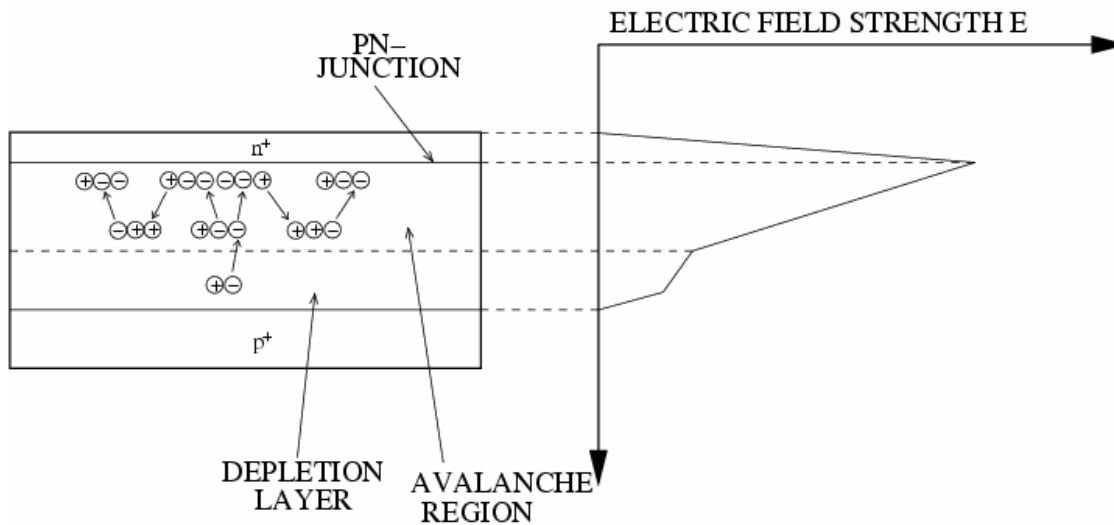
## PWO:

- + **Fast scintillator**
- + **Low cost material**
- **Light yield and energy resolution show strong temperature dependence**

## BGO:

- + **Sufficient light yield**
- + **Light yield and  $\sigma_E/E$  are nearly temperature independent**
- **Slow scintillator**

# APD function



Layout of an Avalanche Photo Diode (APD):  
Photodiode with an additional avalanche region

- Strong internal electric field:
  - Electrons generate electron-hole pairs
  - Avalanche process

# APDs for PANDA



- APDs as readout of the PANDA calorimeter:
  - Operation in strong B fields of 1.5 T
  - First test measurements with PWO and BGO:
    - Qualification of an APD as readout detector
  - Readout area of the crystals: 22 x 22 mm<sup>2</sup>
    - **R**eadout area coverage with an active area of 5 x 5 mm<sup>2</sup> too small
- Development of Large Area APDs (LAAPDs)  
with an active area of 10 x 10 mm<sup>2</sup> (Hamamatsu)

# Requirements for Preamp



## Crystal

- PWO: 390 photons / MeV
- Rate: 300 kHz / Crystal

## APD

- Detector capacity:  $\approx 300$  pF
- Leakage current:  $\approx 50$  nA
- Gain: 50
- QE: 70%

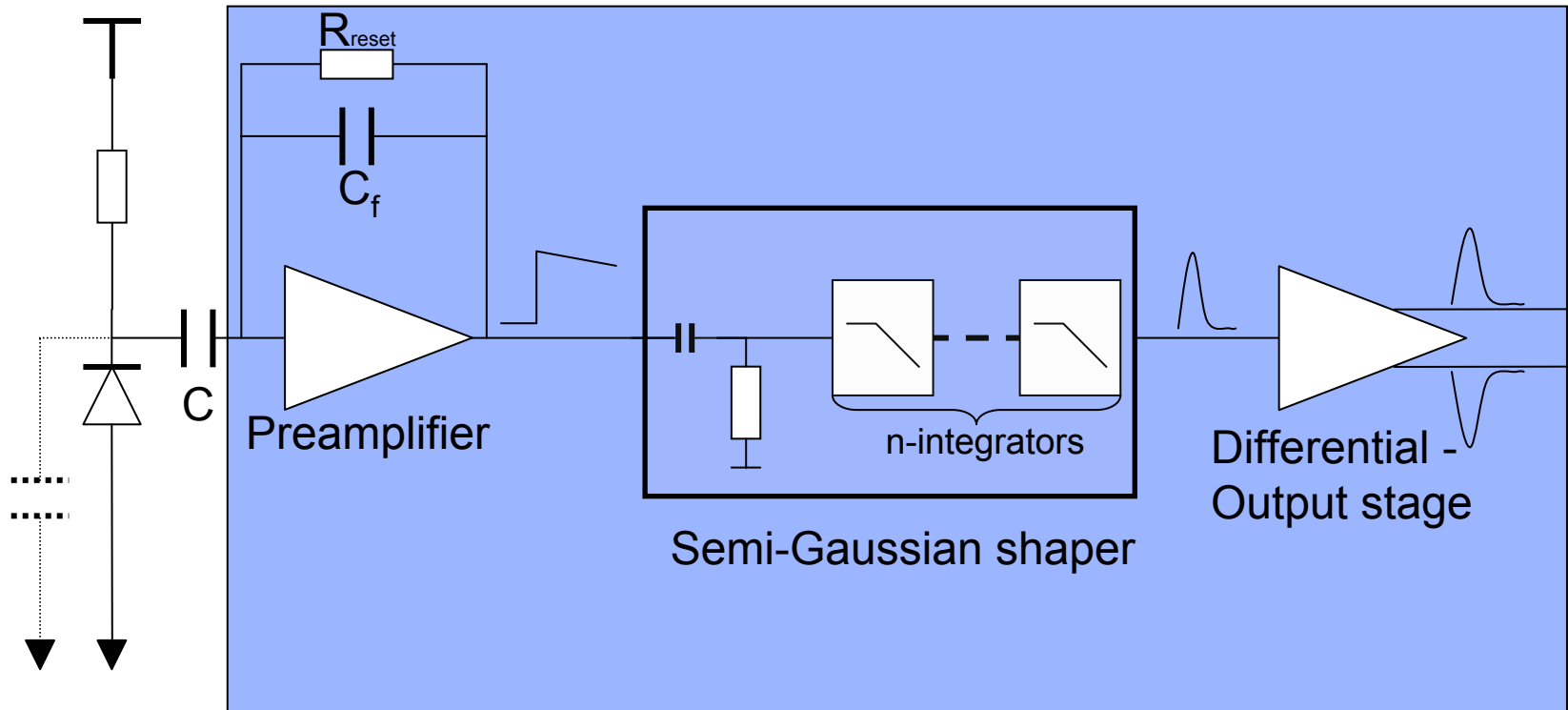
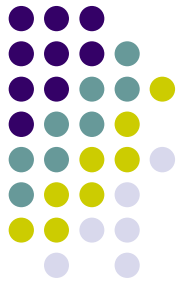
→ **13.000 e<sup>-</sup> / MeV**

## Preamplifier

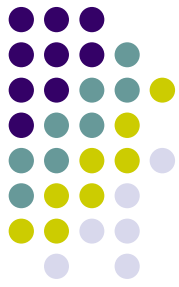
- Dynamic range: 1 MeV – 10 GeV
- Low noise
- Low power
- Compact design



# APD - Readout



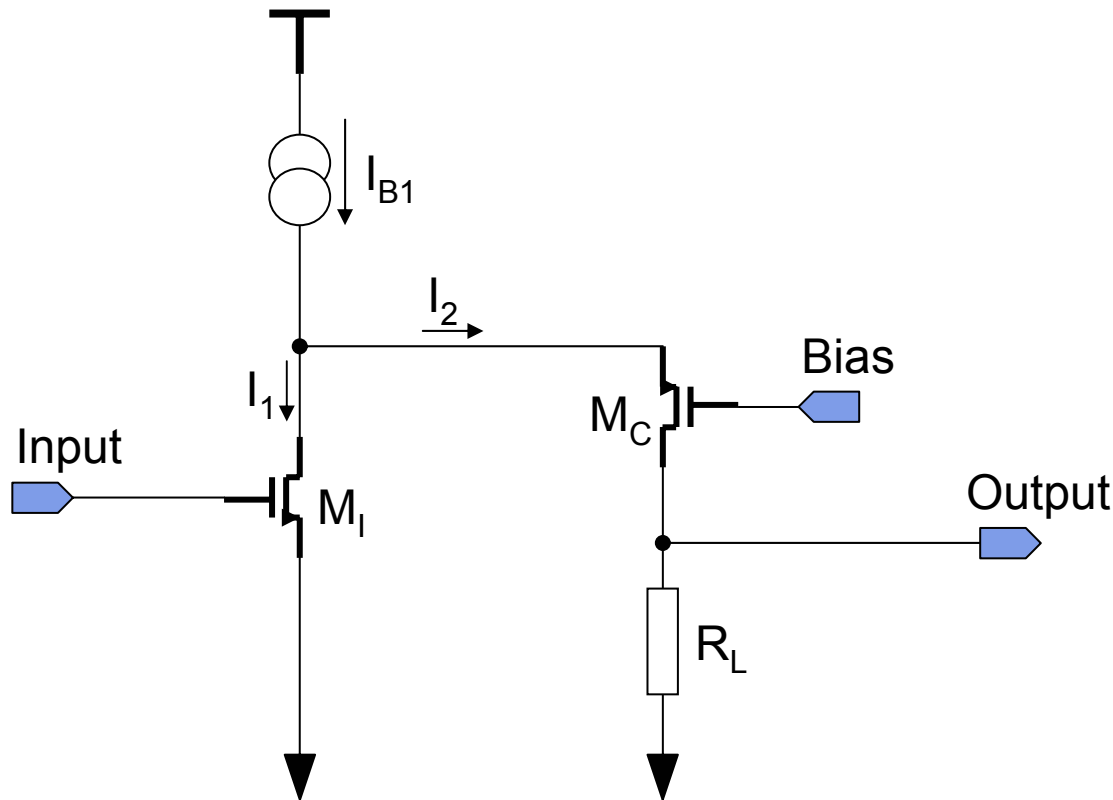
ASIC



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# First Calculations

# Folded Cascode

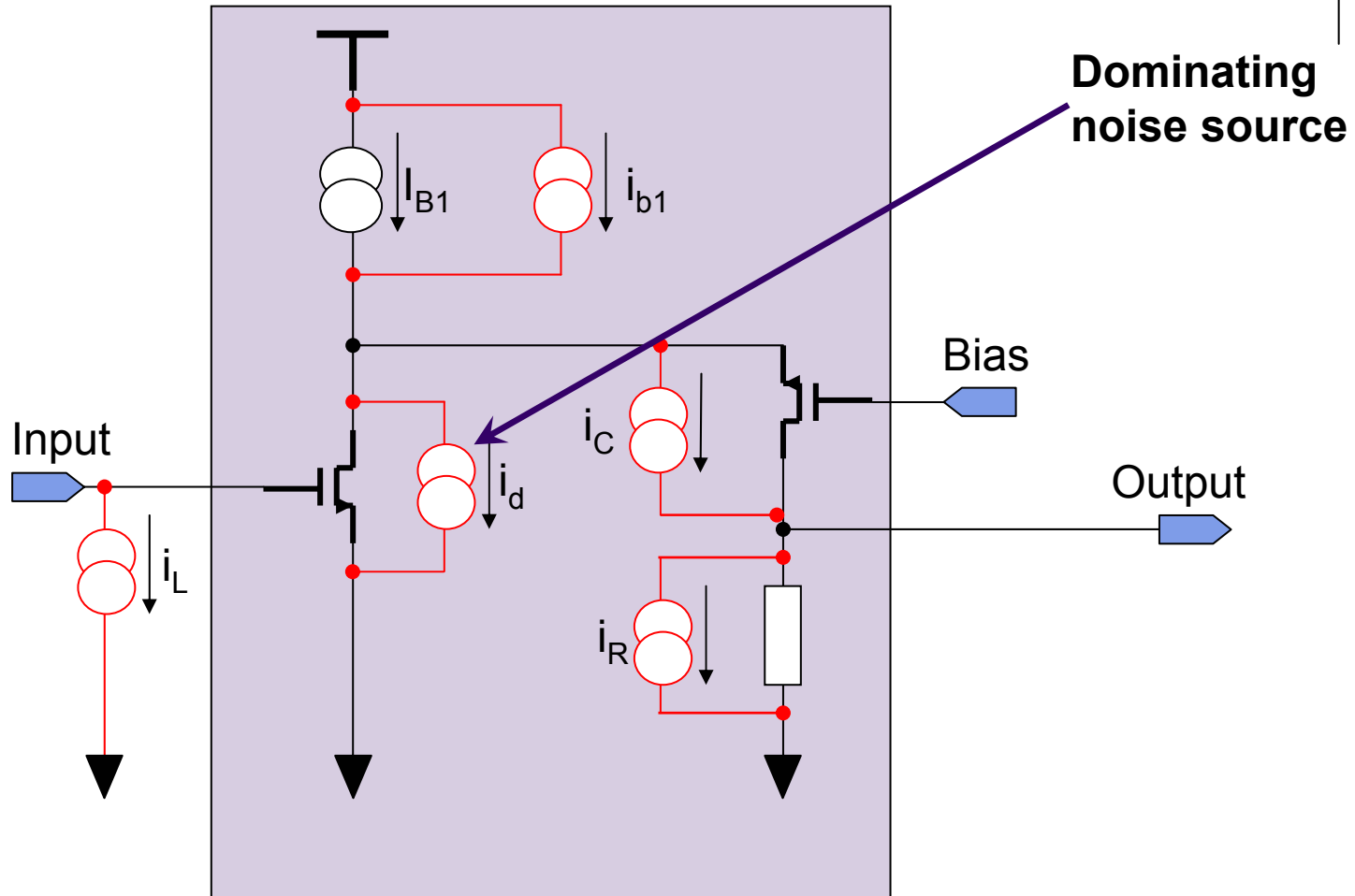
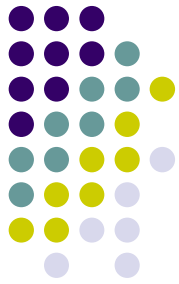


Basic design of the preamplifier: Folded Cascode

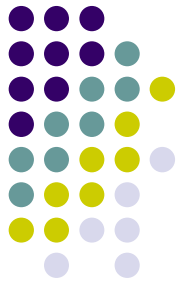
## Operation method:

- $I_{B1} = I_1 + I_2$  choose:  $I_1 = 10I_2$
- Input signal leads to current change
- $I_{B1} = \text{const.} \rightarrow \Delta I_2 = -\Delta I_1$
- $\Delta U_{\text{out}} = \Delta I_2 \cdot R_L$
- $M_C$  decouples input transistor from output signal

# Noise Source



# Noise Sources



**Shot noise due to detector leakage current:**

$$i_L^2 = 2 \cdot q \cdot I_L$$

- $I_L$ : leakage current

**Thermal Noise:**

$$i_t^2 = 8/3 \cdot k_B \cdot T \cdot g_m$$

- $g_m$ : transconductance

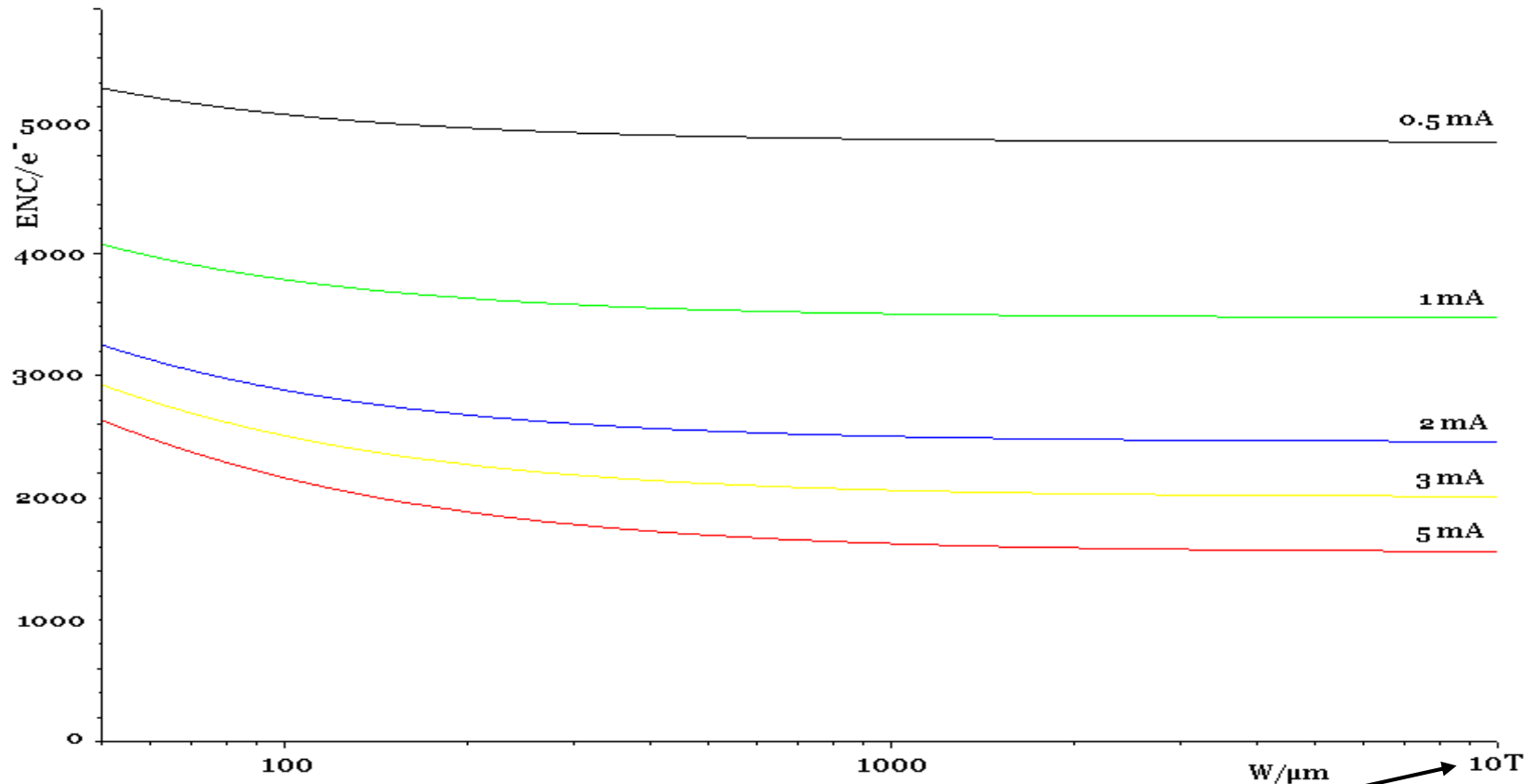
**1/f Noise:**

$$i_f^2 \sim \frac{I_1}{L^2 \cdot f}$$

- $f$ : frequency
- $L$ : gate length

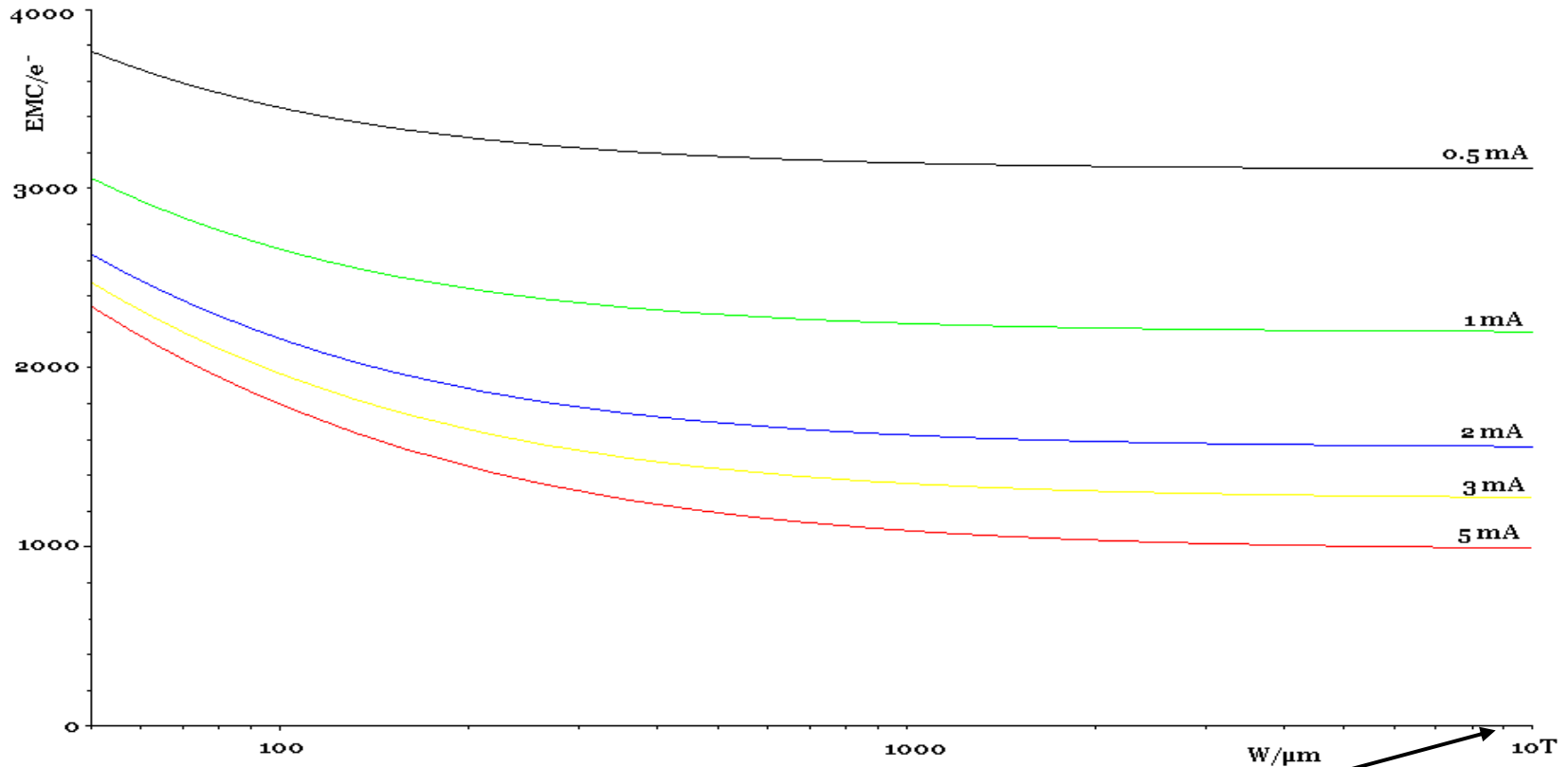
- Noise depends on current
- Noise depends on transistor area

# Preamplifier Noise: $\tau_s = 100\text{ns}$



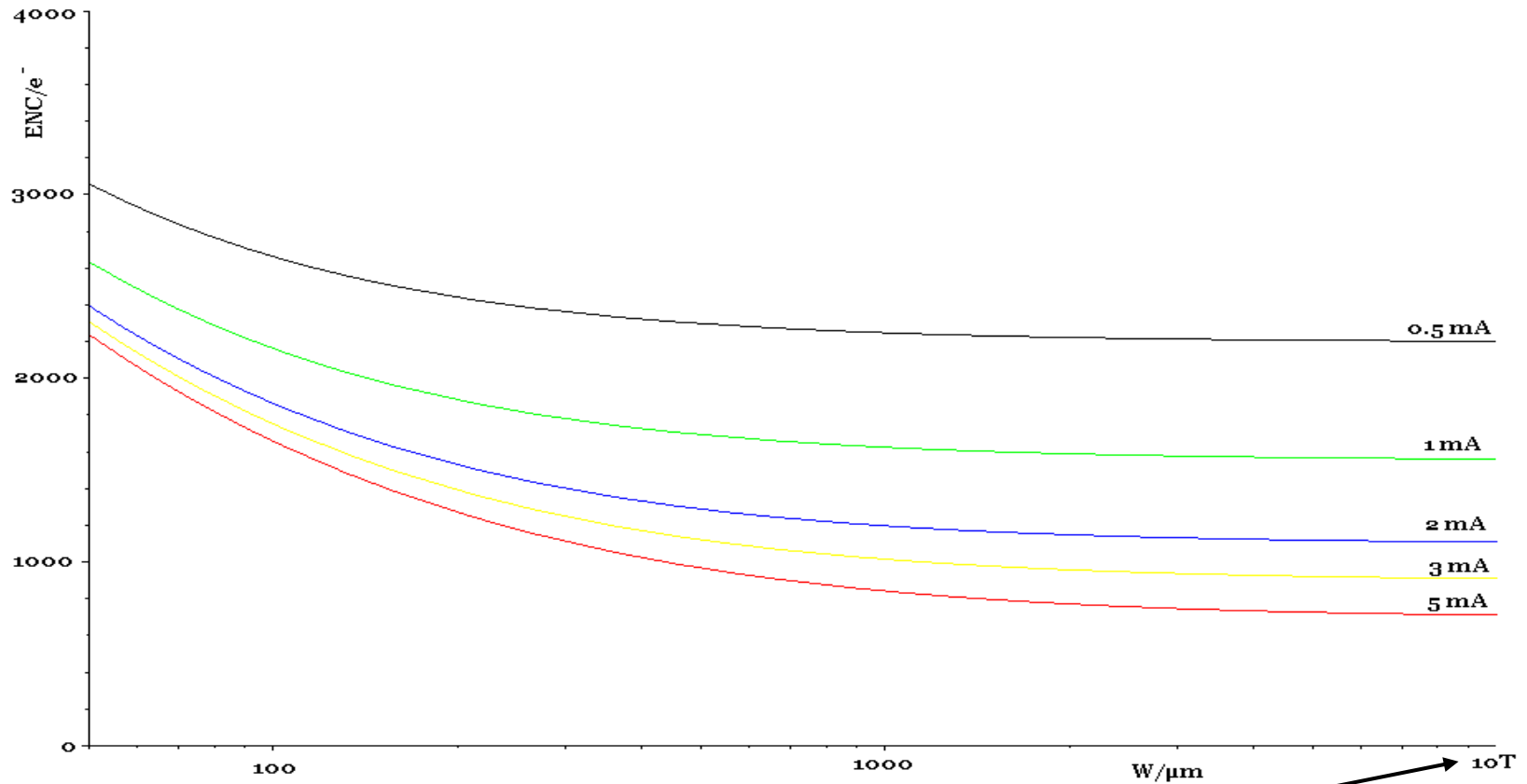
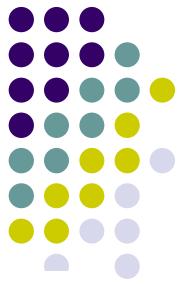
- Width:  $W = 10.000 \mu\text{m}$   
→ ENC  $\approx 2600 e^-$  @ 2 mA

# Preamplifier Noise: $\tau_s = 250\text{ns}$



- Width:  $W=10.000\ \mu\text{m}$   
→  $\text{ENC} \approx 1700\ e^-$  @ 2 mA

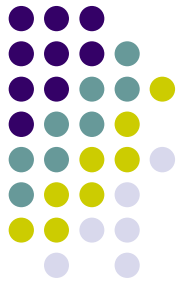
# Preamplifier Noise: $\tau_s = 500\text{ns}$



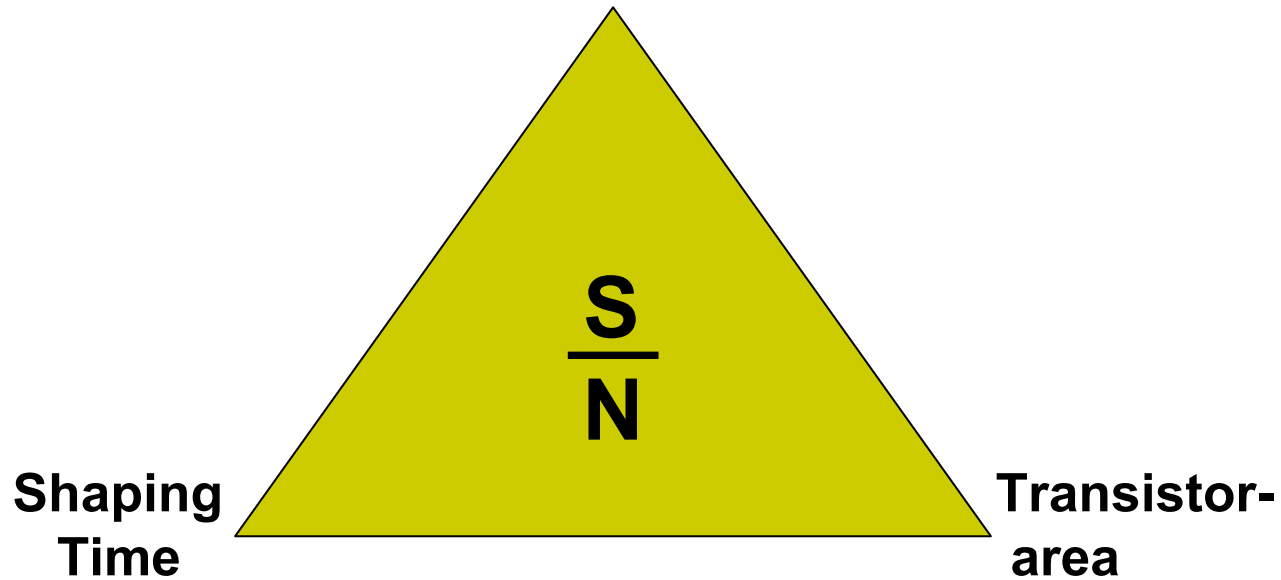
- Width:  $W = 10.000 \mu\text{m}$   
→  $\text{ENC} \approx 1200 e^- @ 2 \text{ mA}$

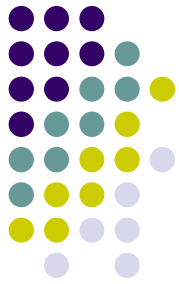


# Signal to Noise



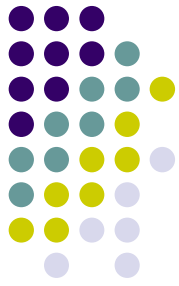
**Current (Power consumption)**



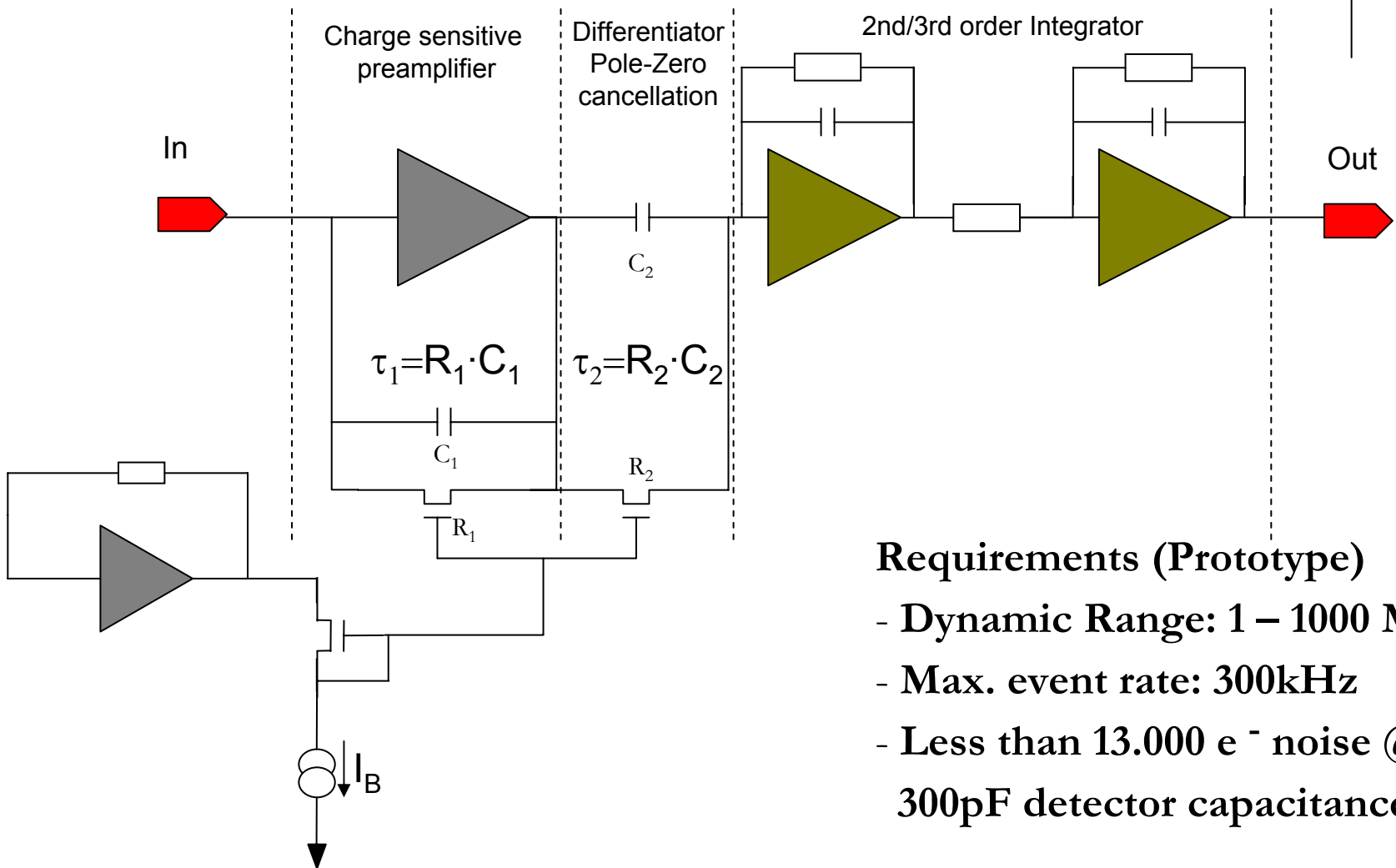


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# First Prototype

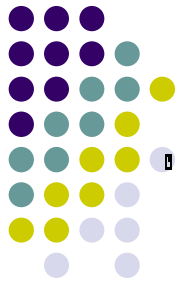


# Readout Path

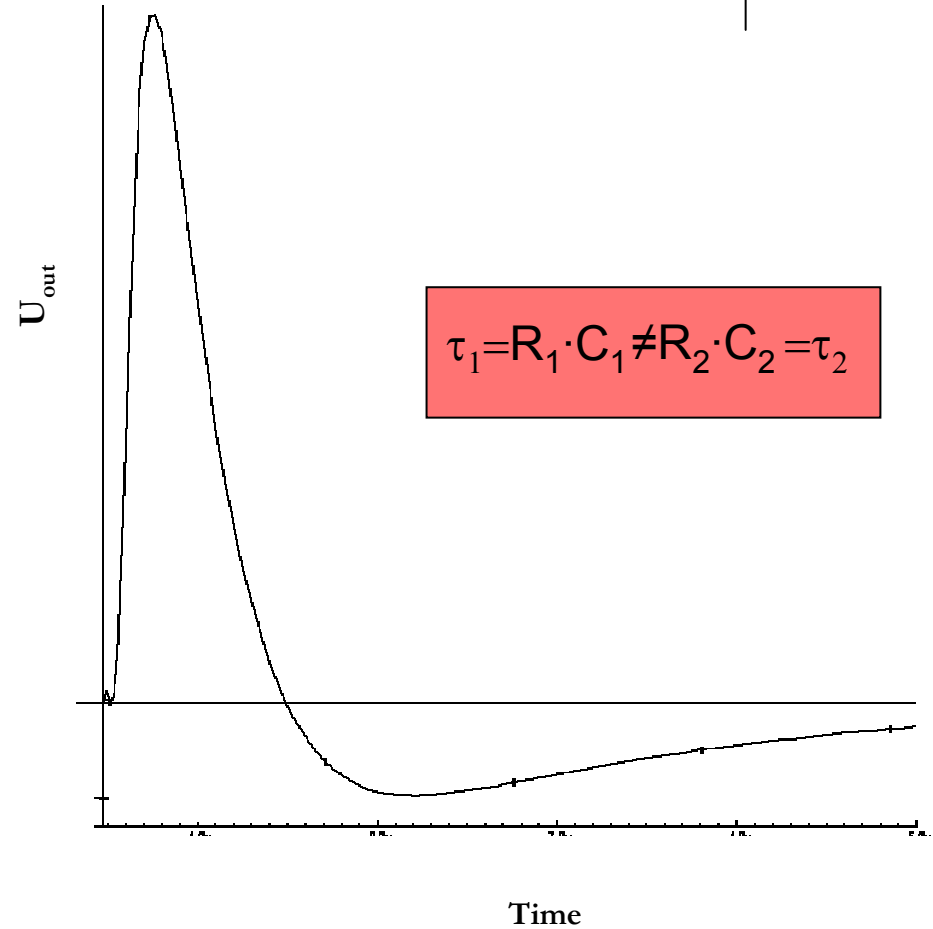
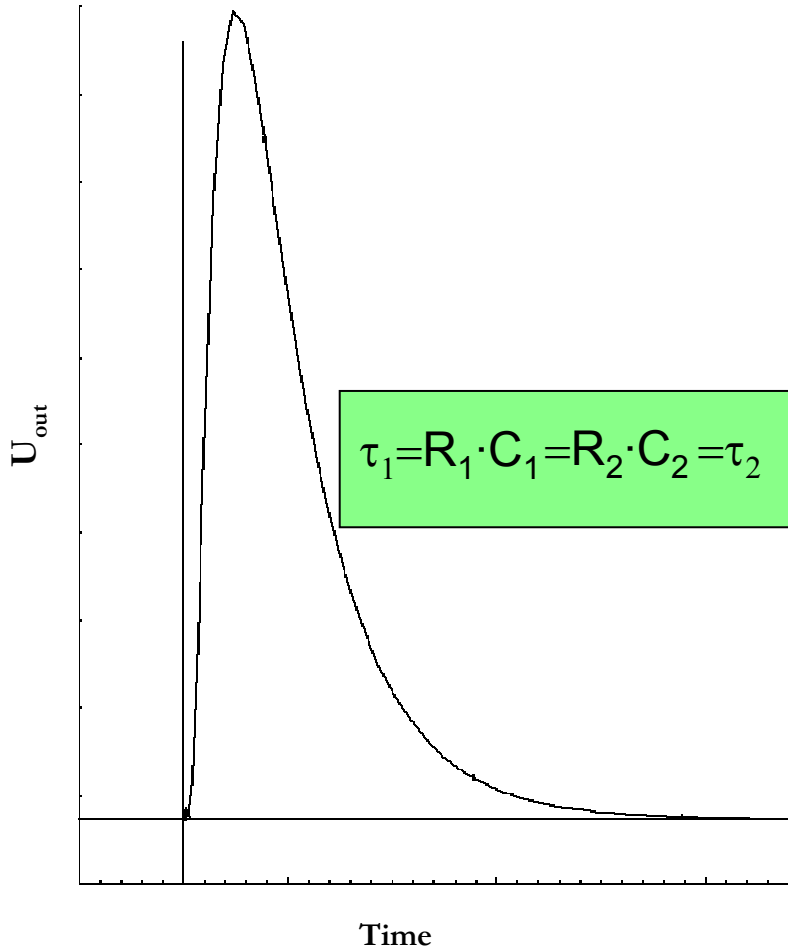


## Requirements (Prototype)

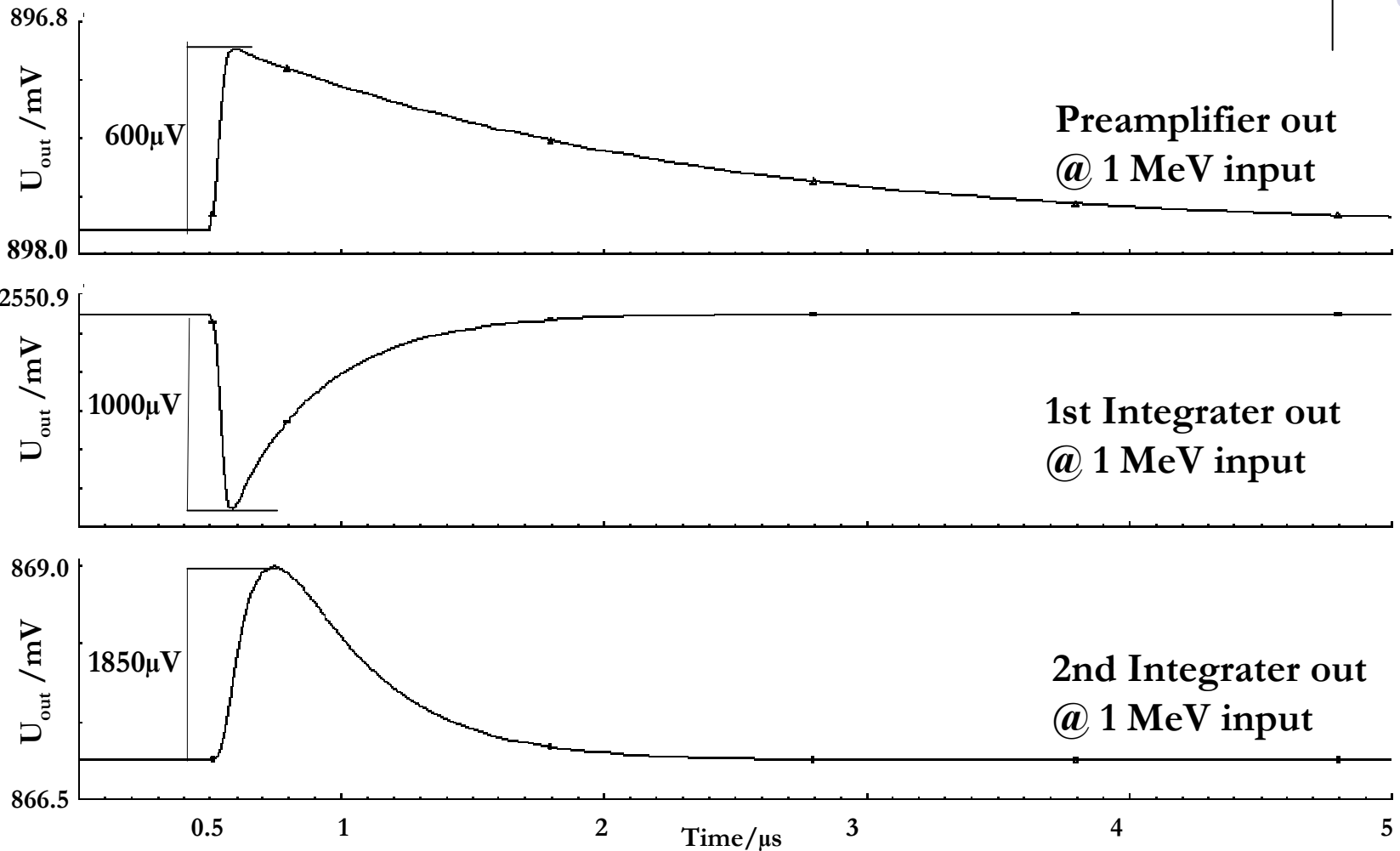
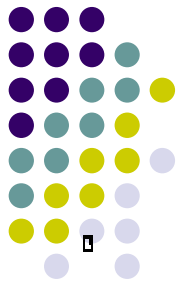
- Dynamic Range: 1 – 1000 MeV
- Max. event rate: 300kHz
- Less than 13.000 e<sup>-</sup> noise @ 300pF detector capacitance



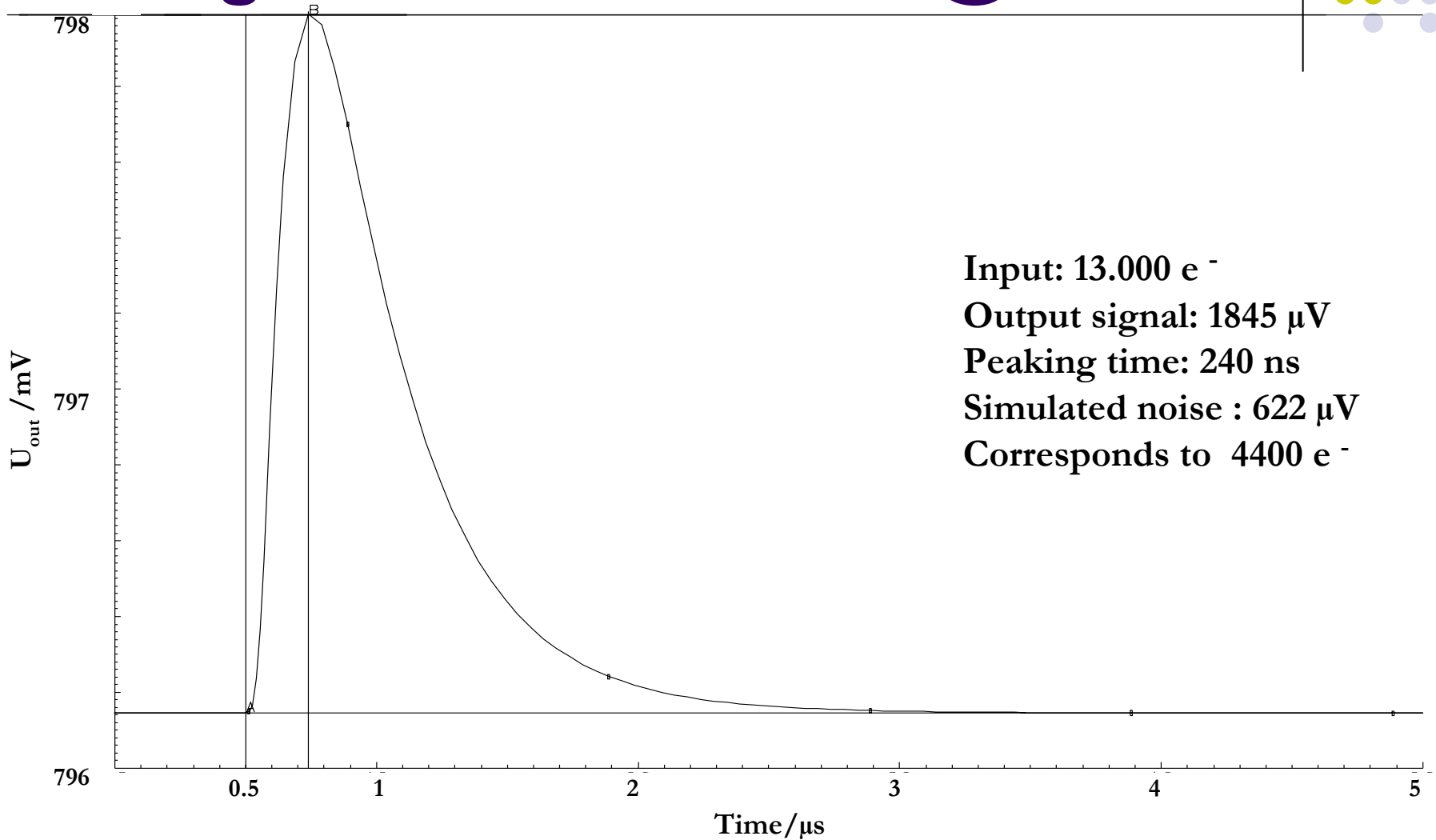
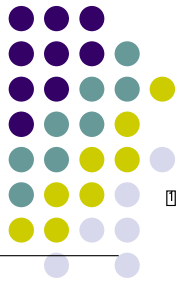
# Pole-Zero Cancellation



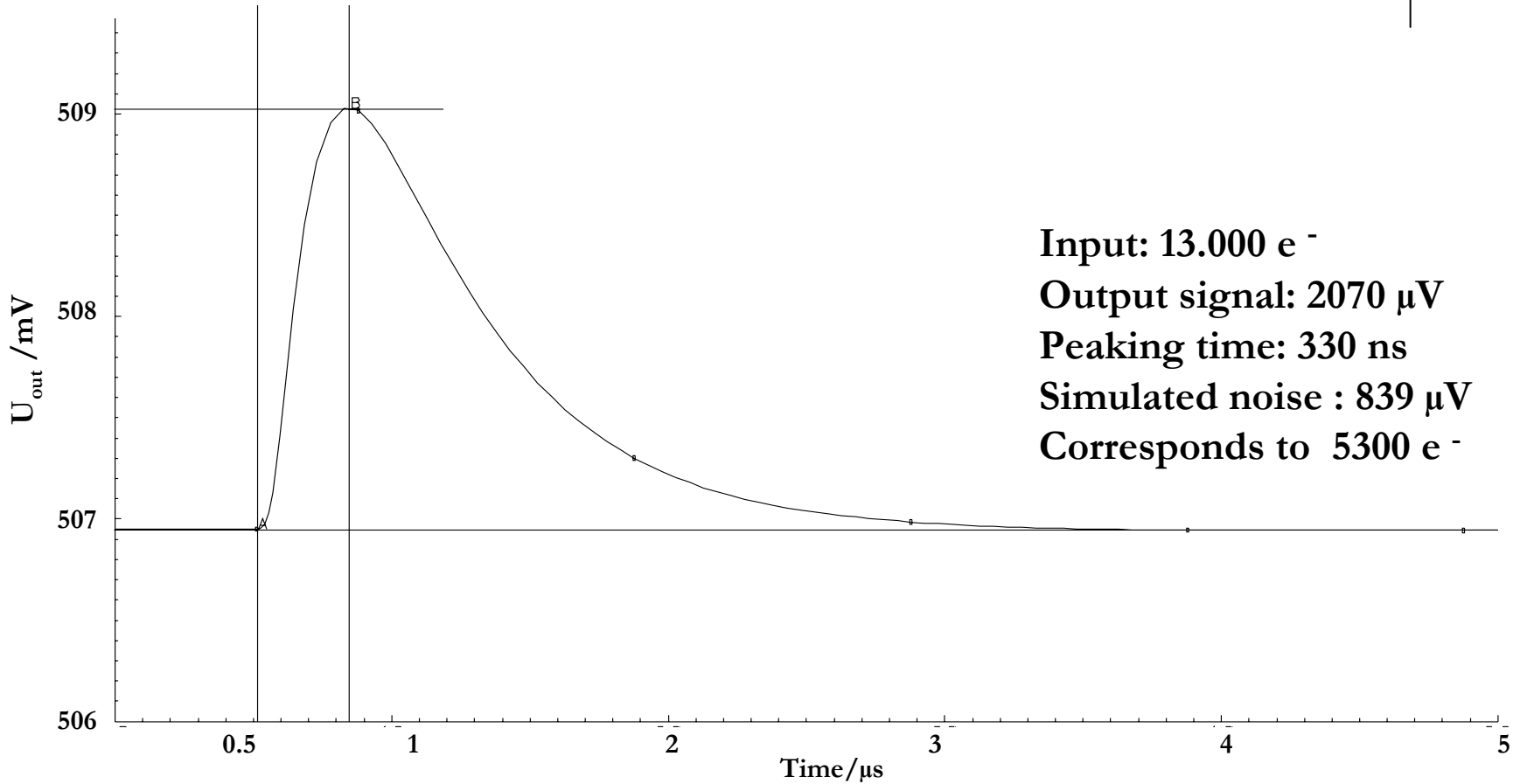
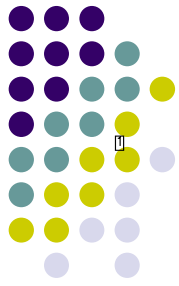
# Transient Simulations @ -25°C



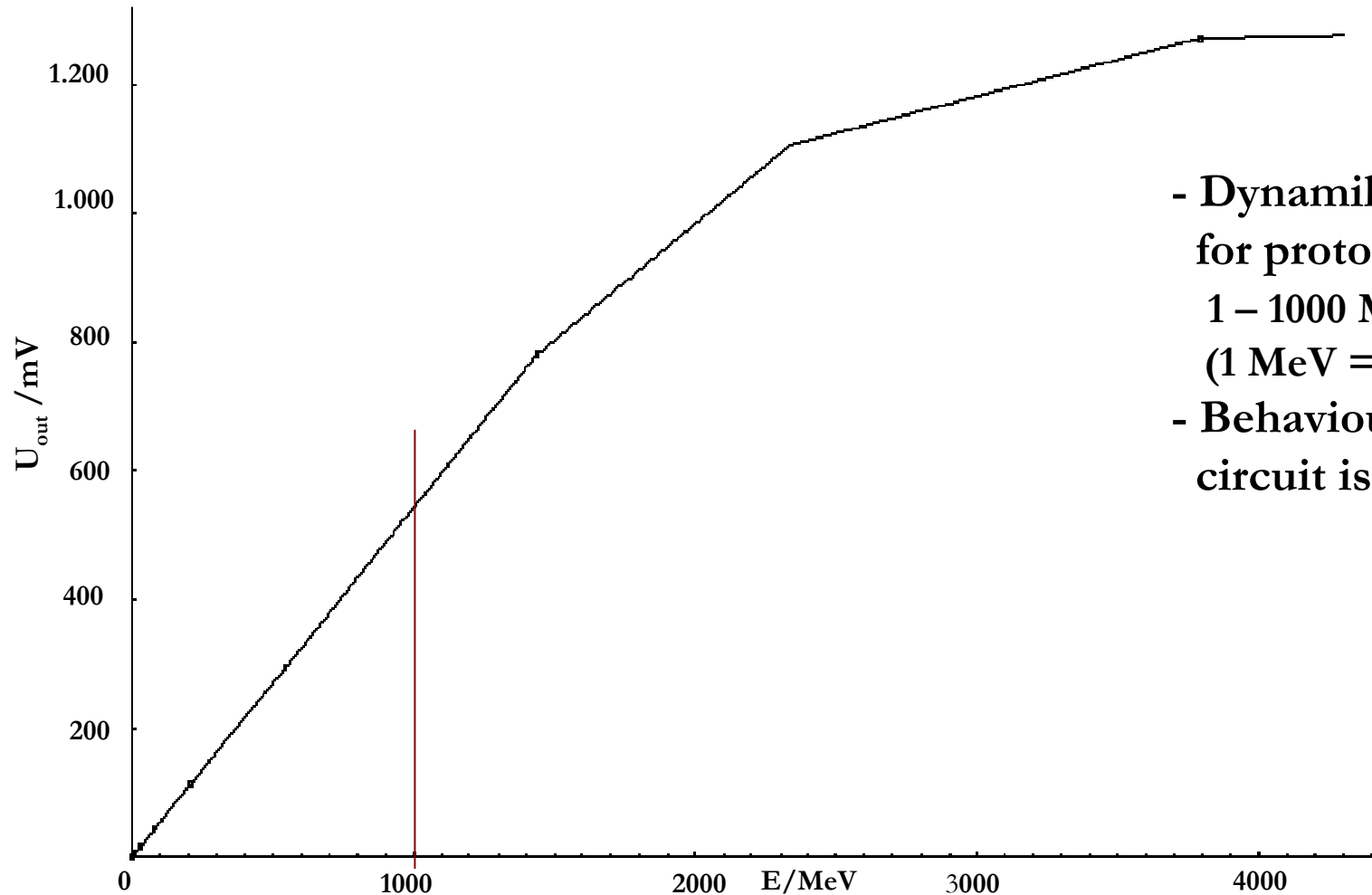
# Signal to Noise Ratio @ -25°C



# Signal to Noise Ratio @ +25°C



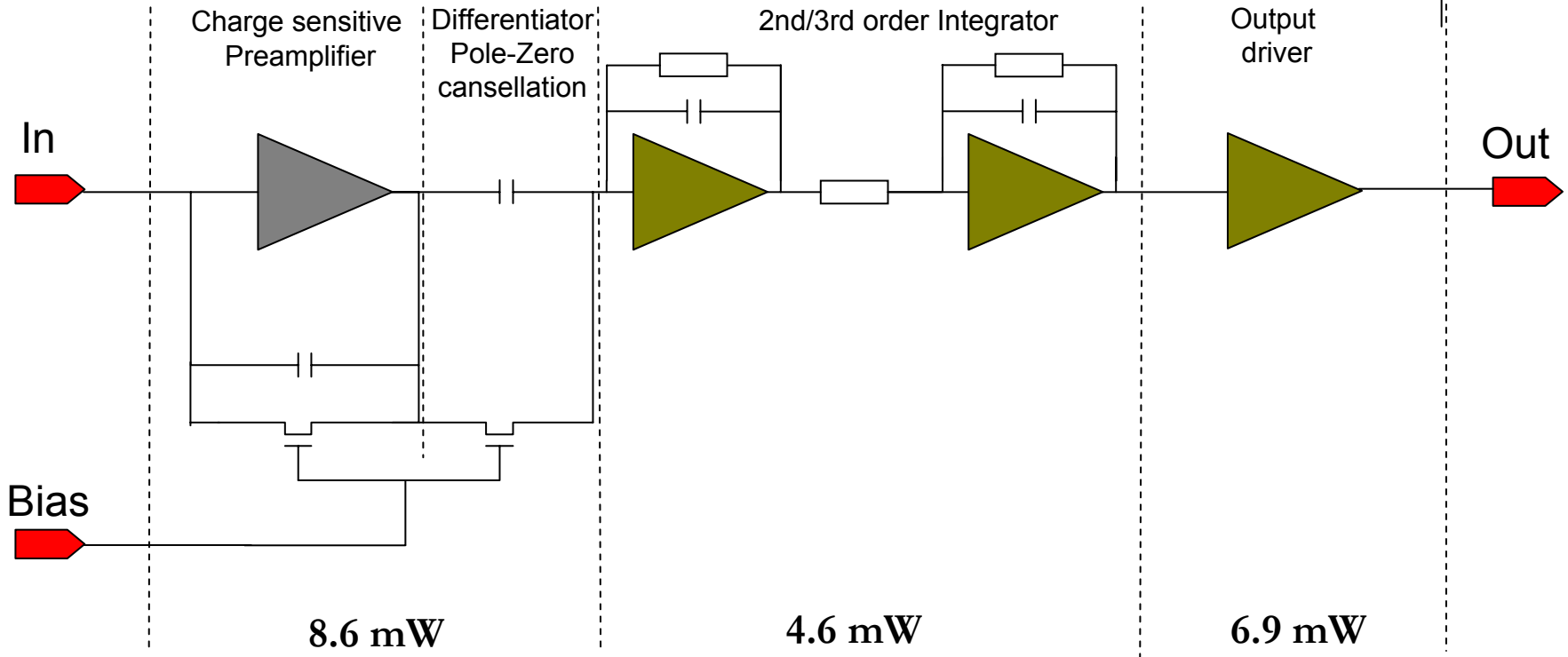
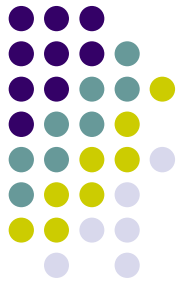
# Linearity



- Dynamik range for prototype:  
1 – 1000 MeV  
(1 MeV = 13.000  $e^-$ )
- Behaviour of the circuit is linear



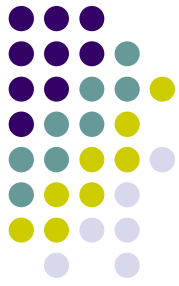
# Power Consumption



**21 mW per channel**

# Summary and Outlook

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- Complete Front-end circuit has been designed and optimized for best noise performance
- Design fulfills noise, rate and power requirements
- Design is robust against process, supply voltage and temperature variations
- Layout has just started

MPW Run – Submission is planned for summer 2006

## Still open Questions:

- Max. input rate
- Radiation background