

# PIPERADE

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PIEGES DE PENNING POUR LES RADIOISOTOPES A DESIR



PIPERADE collaboration :

P. Ascher, P. Alfaut, M. Aouadi, G. Ban,  
B. Blank, K. Blaum, J.- F. Cam, P. Cheveau, P.  
Delahaye, L. Daudin, F. Delalee, P. Dupré, S. El  
Abbeir, M. Gerbaux, S. Grévy, H. Guérin, E. Liénard,  
D. Lunney, E. Minaya-Ramirez, S. Naimi, L. Perrot,  
A. de Roubin, L. Serani, B. Thomas, J.-C. Thomas



MAX-PLANCK-INSTITUT  
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INSTITUT DE PHYSIQUE NUCLEAIRE  
ORSAY



CSNSM

- SPIRAL2 & The DESIR facility
- The PIPERADE set-up
  - GPIB (General Purpose Ion Buncher)
  - Double Penning Trap
- PIPERADE @ CENBG
- SPIRAL2 control system & Automation
- PIPERADE Control Command
- GPIB Equipments & Control System Architecture
- Trap Equipments & Control System Architecture

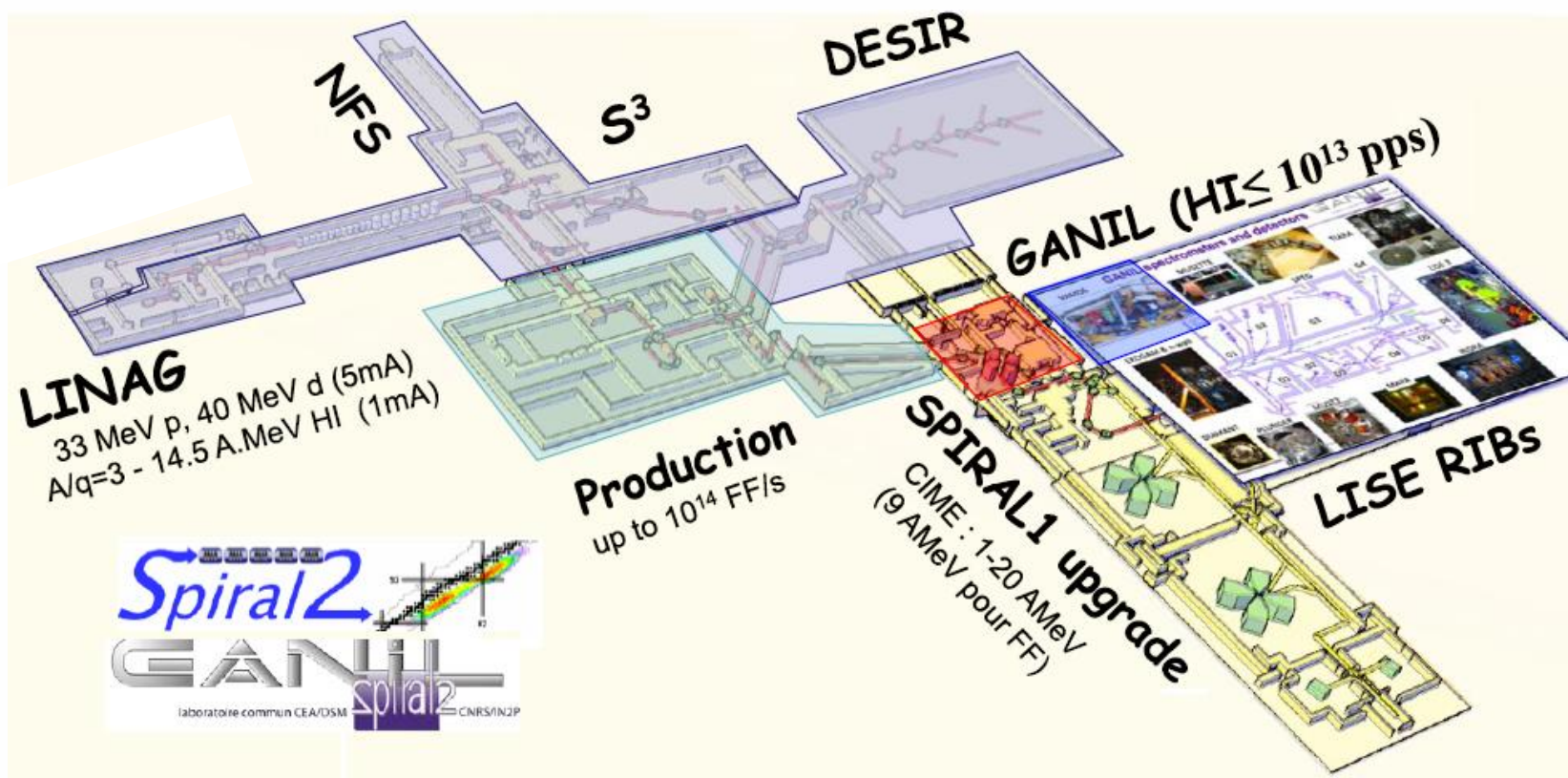


# SPIRAL2 @ GANIL

**Phase 1** LINAC + NFS + S3 → Under construction ( Commissioning 2016 ...2017 )

**Phase 1+** DESIR → Construction mid-2017 ( Commissioning mid- 2019 )

**Phase 2** Production building → > 2025?

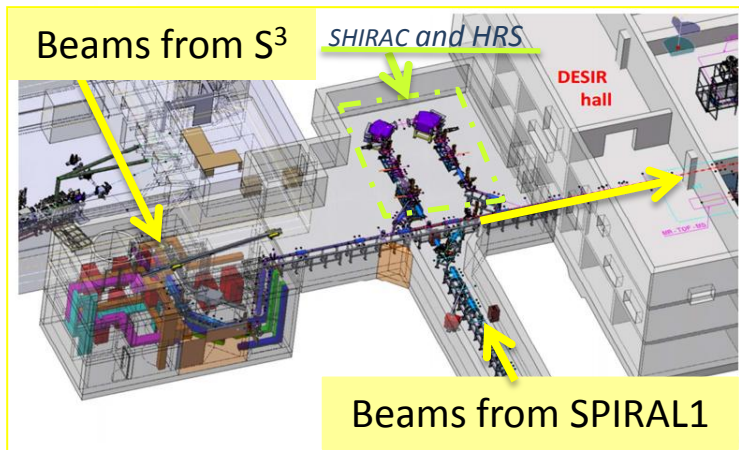


Collaboration spokesperson: *B. Blank, CENBG*

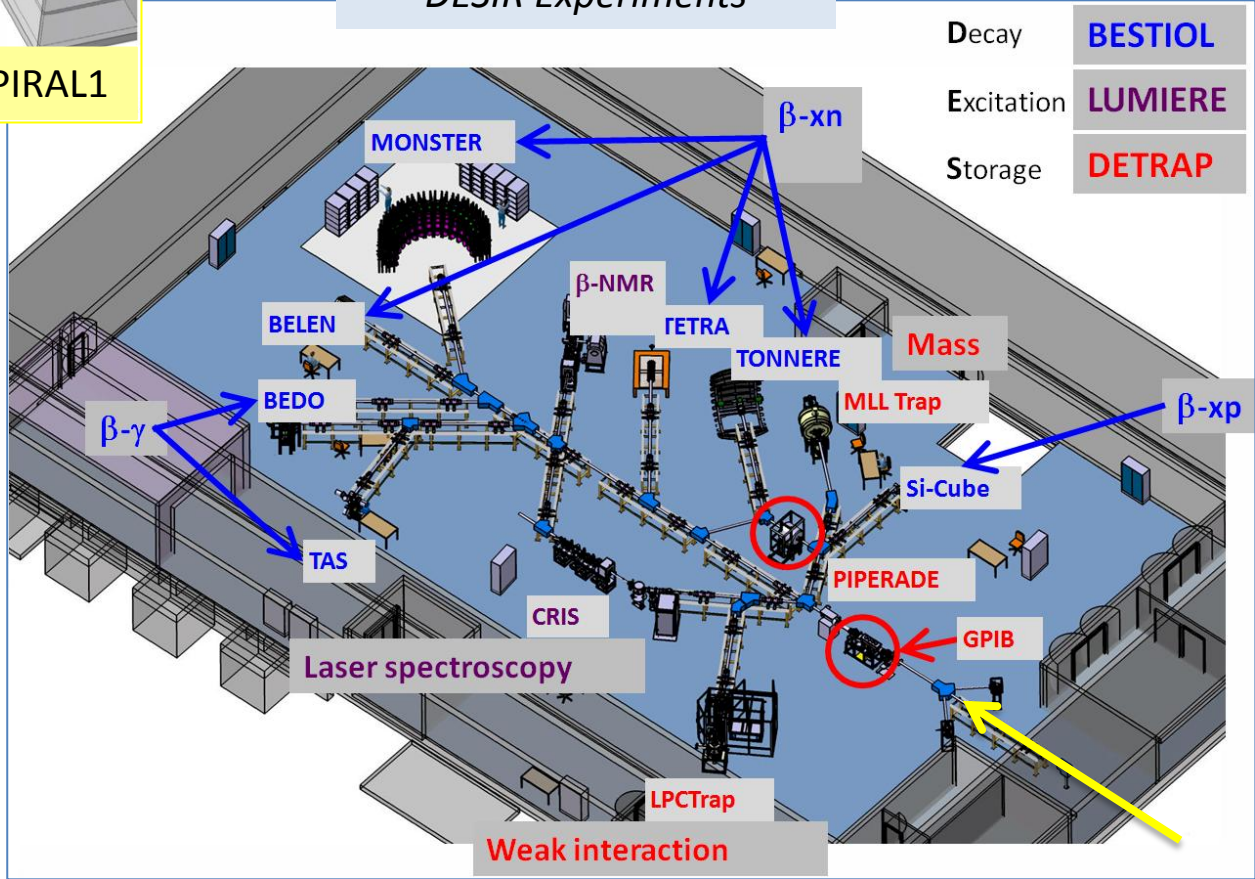
Facility coordinator: *J.-C. Thomas, GANIL*



# SPIRAL2/DESIR beams & Experiments



## DESIR Experiments



Decay	BESTIOL
Excitation	LUMIERE
Storage	DETRAP





**Main Goal** : Delivering very pure and **large samples** of exotic nuclei to the DESIR setup

## Requirements

→ **Mass resolution**  $> 10^5$  (to clean isobars not cleaned by the HRS + isomers)

Ions of interest usually less produced than contaminants

→ Purification of large samples ( $> 10^4$  ions/bunch)

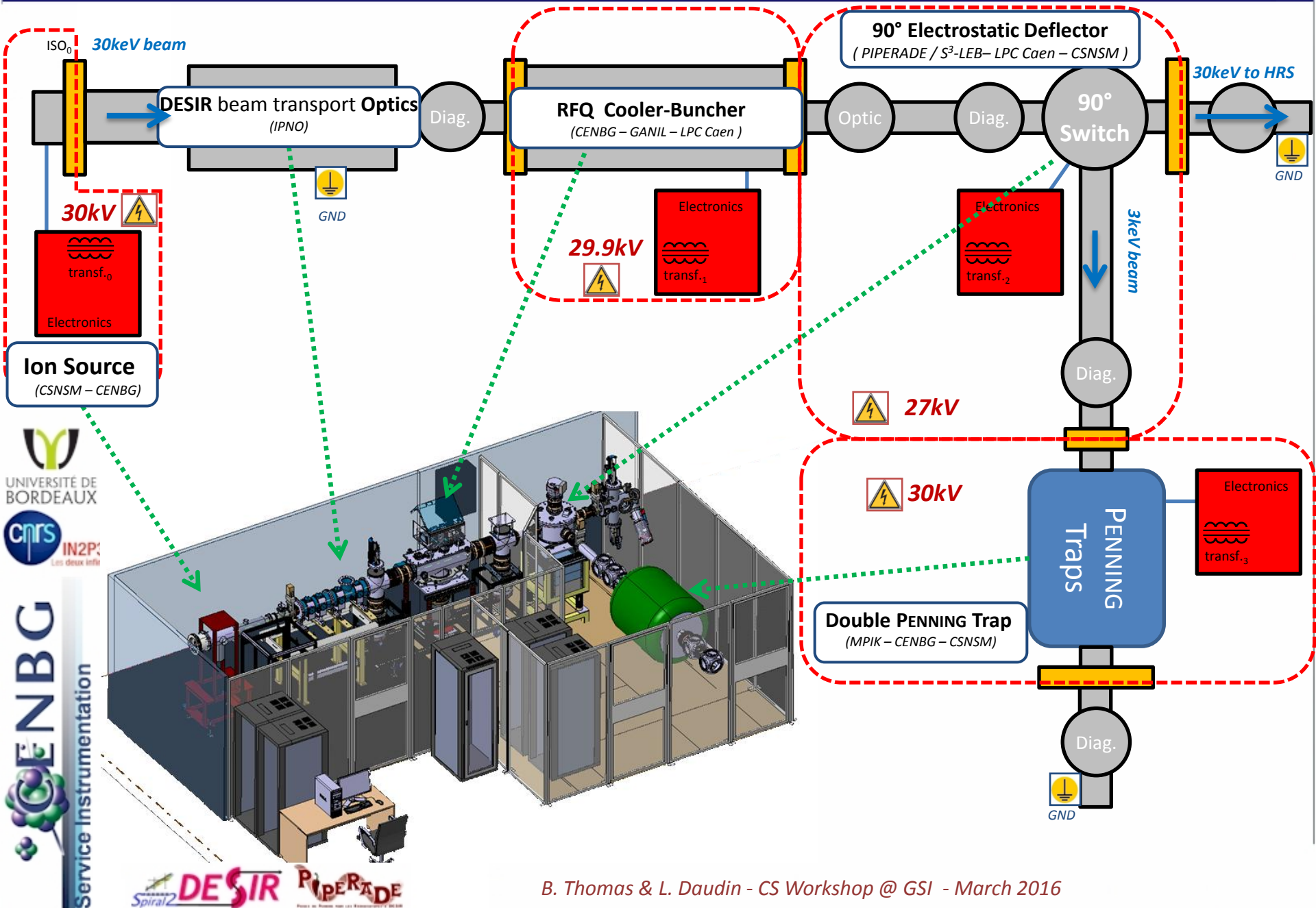
→ "Fast" cleaning process

→ High transmission efficiency

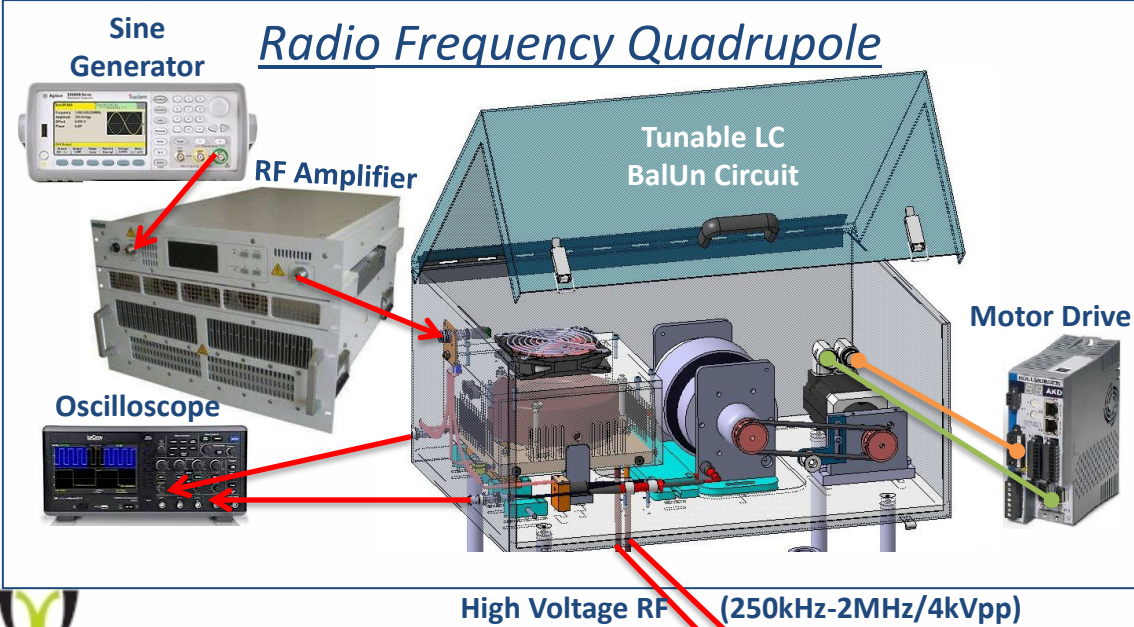
→ Double PENNING trap (1 for purification, 1 for accumulation)

**but require a cooled & bunched ion beam :**

→ RFQ Cooler-Buncher « GPIB » (General Purpose for Ion Buncher)

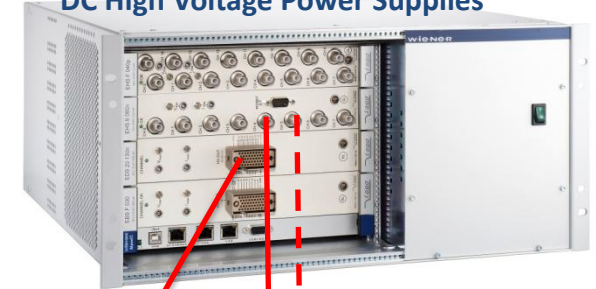


Radio Frequency Quadrupole



Electrostatic fields (32 HV PS)

DC High Voltage Power Supplies

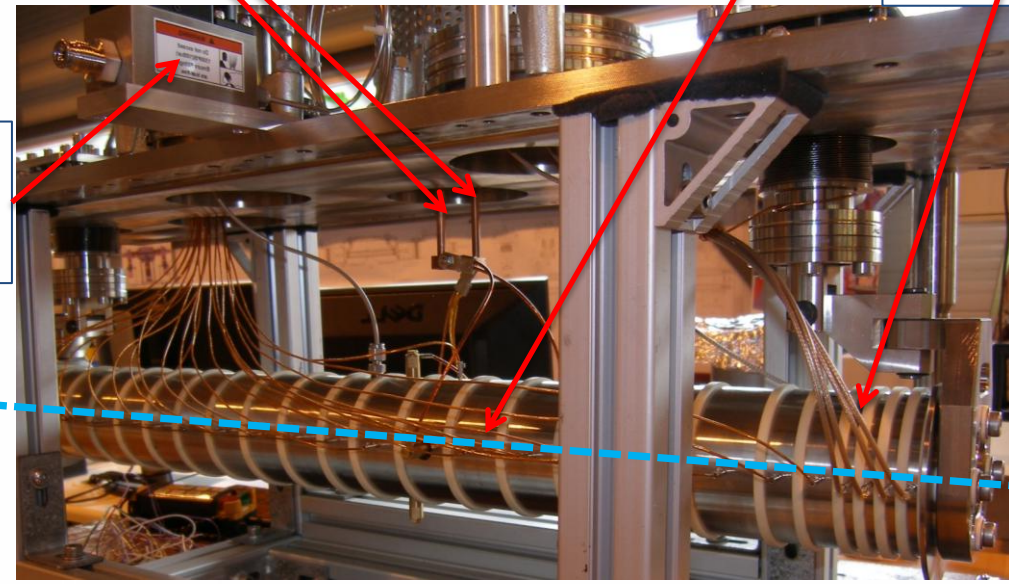


Buncher

Bunch Controller



Fast HV switches



Cooler

Buffer gas He  
Mass Flow controller

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CNRS

IN2P3

GENBG

Service Instrumentation

DESIR

PIPERADE





Clients : OPI (Operator Programming Interface)  
 HMI (Human Machine Interface) on Linux PC

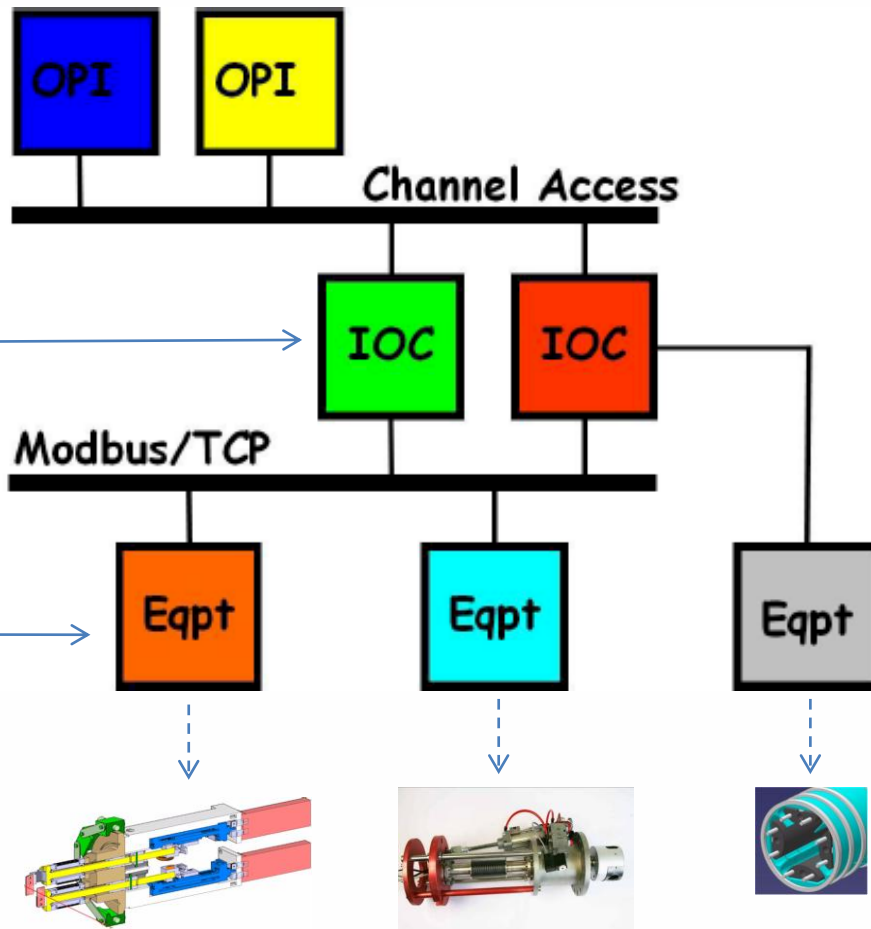
Communication : Ethernet Network  
 Protocol: **Channel Access**  
 (publish & subscribe Processed Variables) → PV

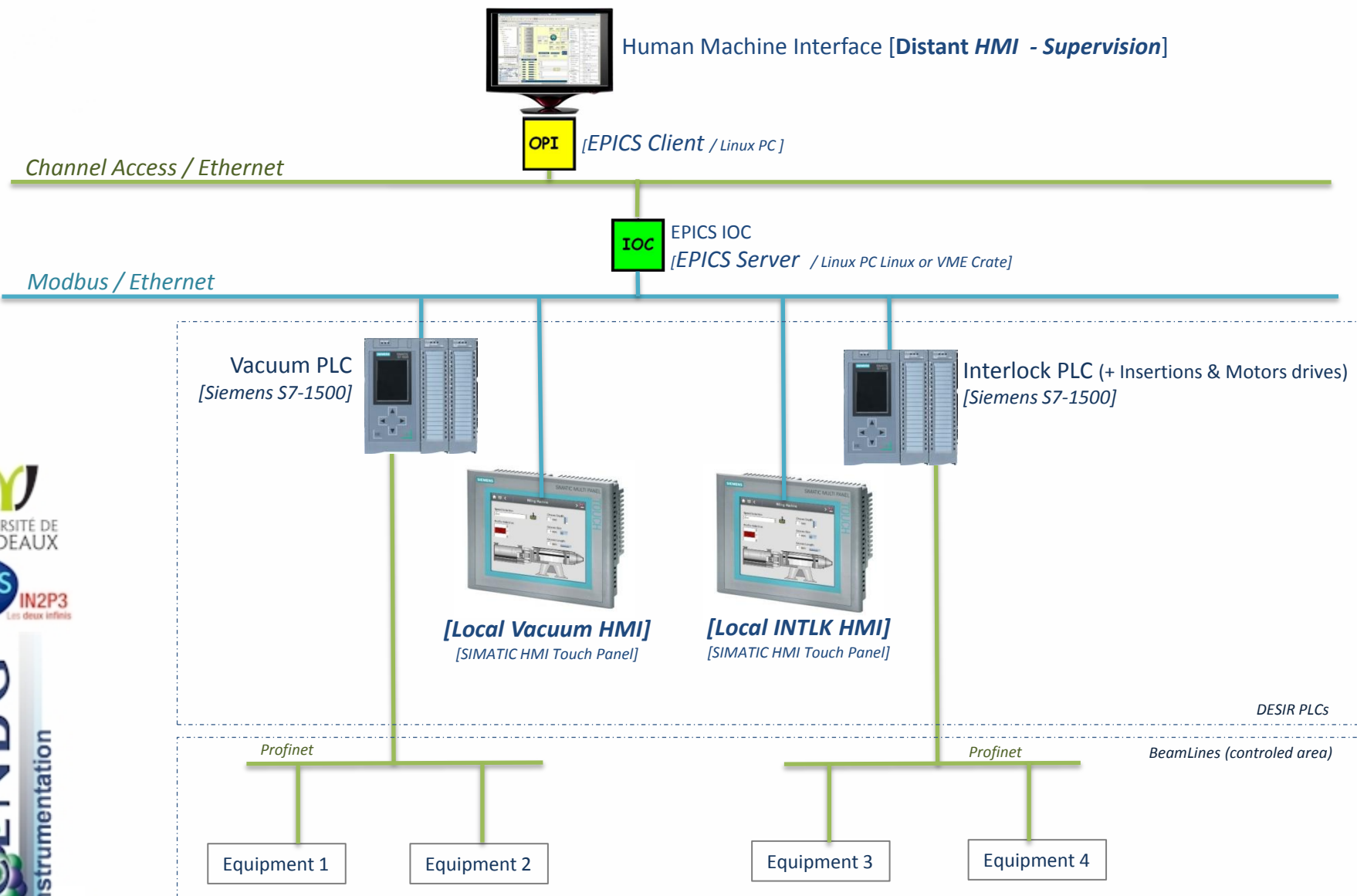
Servers : IOC (Input-Output Controller)  
 Real Time IOC (VME crate / VxWorks) or Soft IOC (Linux PC)

FieldBus/ (Ethernet)  
 Modbus/TCP Protocol

Equipments :

- High Voltage Power Supplies (Optics – Steerers)
- High Current Power Supplies (Dipoles)
- Beam Diagnostics (FC ; Profilers ; Emittancemeter ; Si )
- Motors (Slits)
- Industrial Programmable Logic Controllers (PLCs)



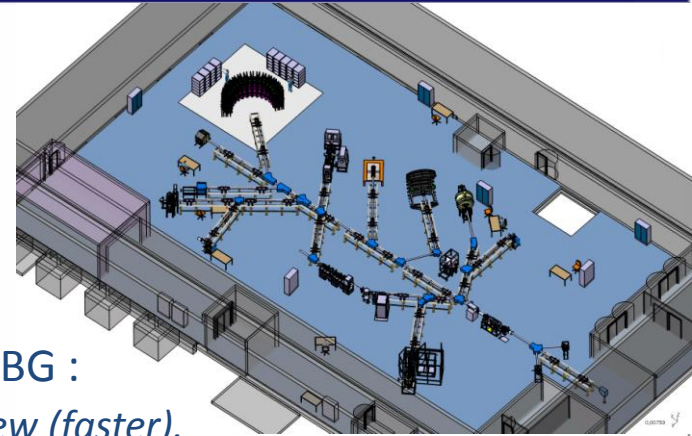




Main DESIR Beam Line CC (RFQ-CB & HRS)

→ EPICS @ DESIR

- PIPERADE Ion source, Optics & RFQ-CB CC @ CENBG :
  - Developed & Running since 2013 with LabView (faster).
  - Equipments & architecture are SP2 EPICS compatible.
  - Migration to EPICS after HRS CC developments.
  - HRS EPICS CC started end 2015 @ CENBG

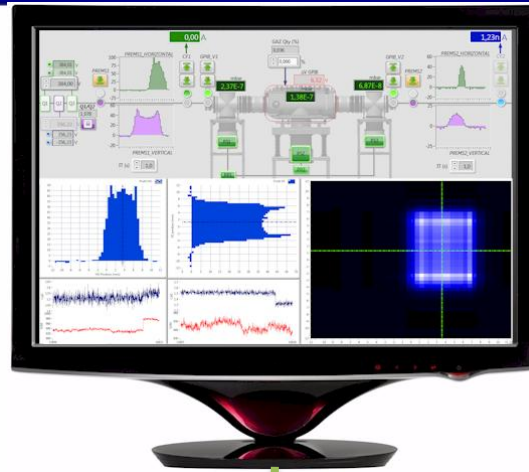
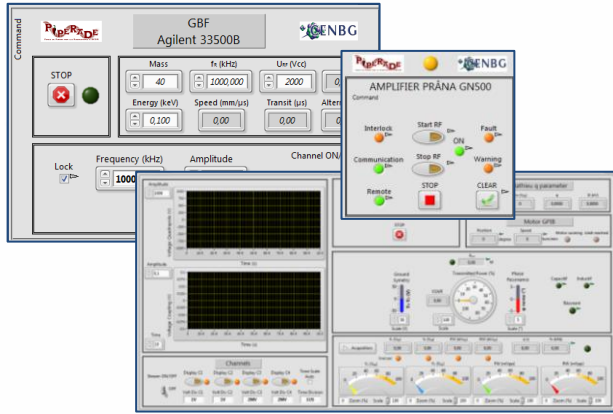


Experiments like PIPERADE PENNING TRAPS could be EPICS controlled **or not**

- Specific needs (Timing sequences, Scans, Data acquisition )
- Other Traps with same need are running CS Framework.
- CS Framework based on LabView Programming Language.

→ CS Framework (GSI) for PIPERADE TRAP Control System.





Ethernet

Vacuum PLC



Ethernet Switch

Fiber Optic



Ethernet Switch

High Voltage Platform (PRAD-GPIB) 30kV

Profinet

SNMP

TCP

RS232 (Power ON/OFF & Status)

Modbus TCP

TCP

VICT

PLC distributed I/O



Cooler



High Voltages



Buncher



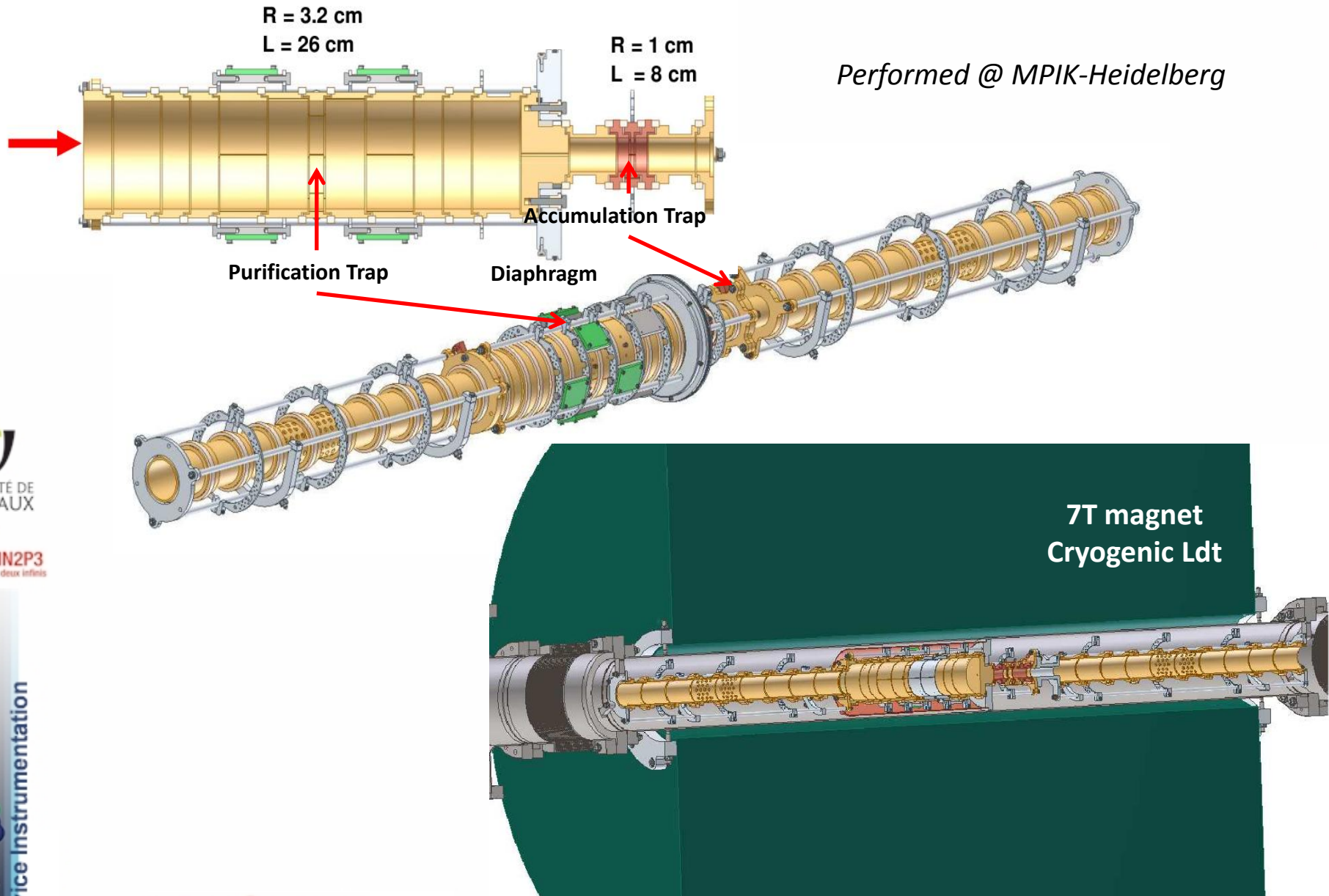
Radio Frequency Quadrupole



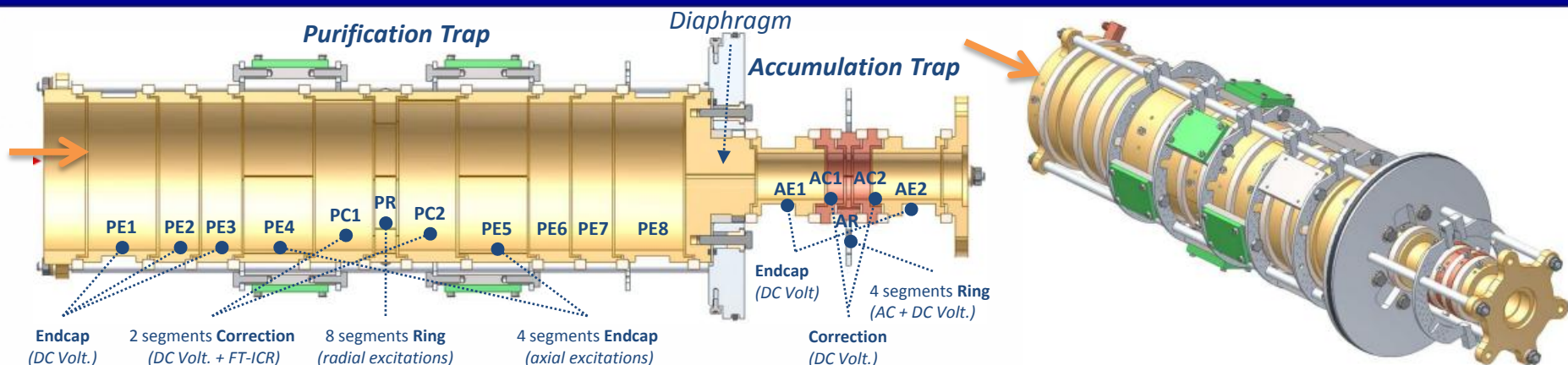
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# DESIGN of the Double PENNING Trap







### Optics & Trapping Voltages

→ DC Power Supplies to control

### Dipolar, Quadrupolar, ... Excitations

→ Function Generators (Freq, Time, Amplitude & Phase)

### FT-ICR Detection

→ Transient recorder + FFT Analyzer

### Programmable Timing Sequence

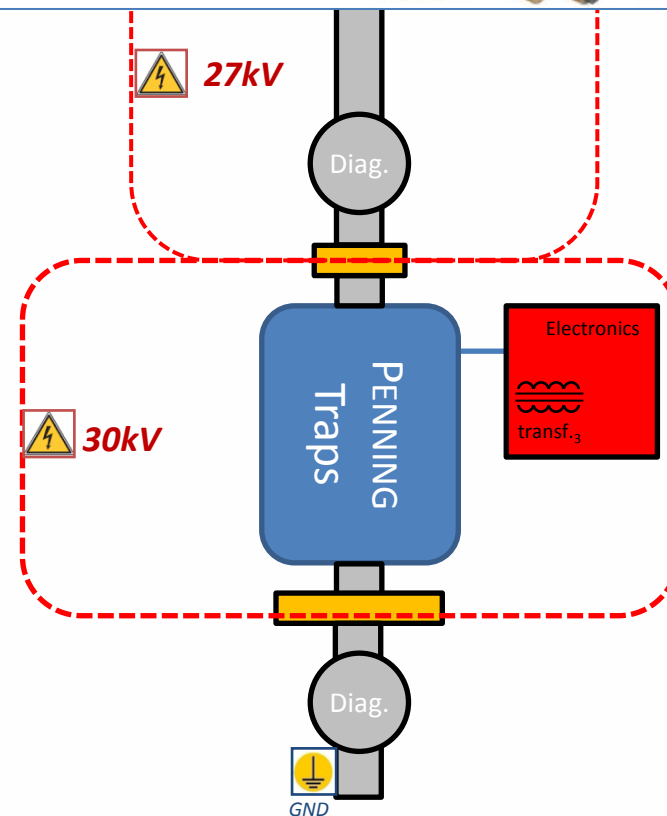
→ Pulse Pattern Generator (PPG) : triggers

### MCP Detection

→ Counting & Time of flight

### Faraday Cup

→ Beam Current Measurement

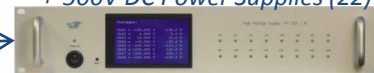


**Optics & Trapping Voltages**  
→ DC Power Supplies & HV Switches to control

ISEG Multichannel Crate (1)



Stahl Electronics Low noise  
±500V DC Power Supplies (22)



Stahl Low noise Switches (15)



Agilent Waveform Generators (5)



**Dipolar, Quadrupolar, ... Excitations**  
→ Function Generators

**FT-ICR Detection**  
→ Transient recorder + FFT Analyzer

SPECTRUM  
4 transient recorders/ PCI card  
16bit ADCs  
130MS/s  
2GSample Memory

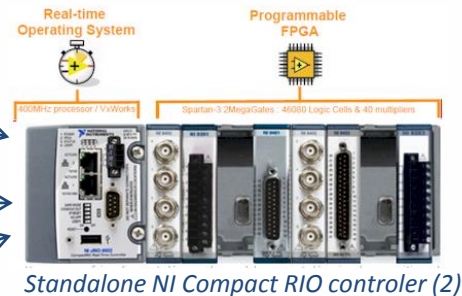


**Programmable Timing Sequence**  
→ Pulse Pattern Generator (PPG)

SHIPTRAP & ISOLTRAP  
RIO PPG software on

**MCP Detection**  
→ Counting & Time of flight

**Faraday Cup**  
→ Current Measurement



Standalone NI Compact RIO controller (2)



## CS Framework



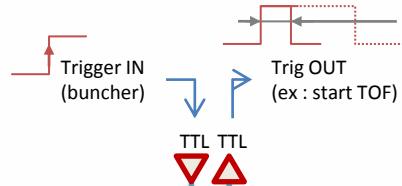
Ethernet (LAN)

CRIO Diag (FC-MCP)

Ethernet Switch



Fiber Optics



High Voltage Platform (PRAD-TRAP) 30kV

Ethernet

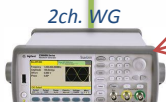
Ethernet Switch



PC with 4 Transient Rec.

USB

USB



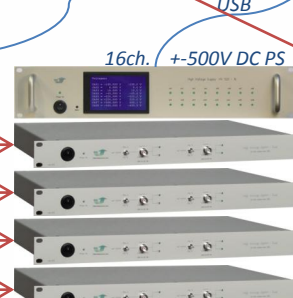
2ch. WG  
PR V<sub>C1</sub> (quad. cycl.)  
PR V<sub>C2</sub> (oct. cycl.)



2ch. WG  
PE V<sub>2</sub> (axial dipol.)  
PE V<sub>2q</sub> (axial quad)



16ch. +500V DC PS  
4 x 2ch. Fast switches



16ch. +500V DC PS  
4 x 2ch. Fast switches



FT-ICR



2ch. WG  
AR V<sub>c</sub> (quad. cycl.)



2ch. WG  
AR V<sub>-</sub> (dipole mag.)  
AR V<sub>+</sub> (dipole mod. cycl.)

PT excitations

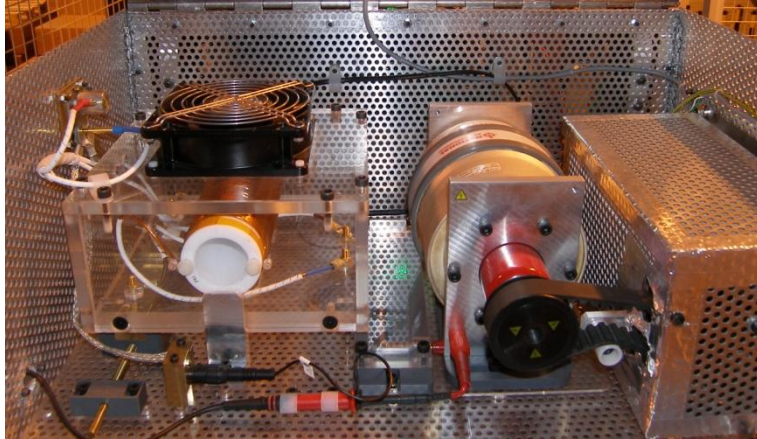
Optics & Trapping Voltages

AT excitations

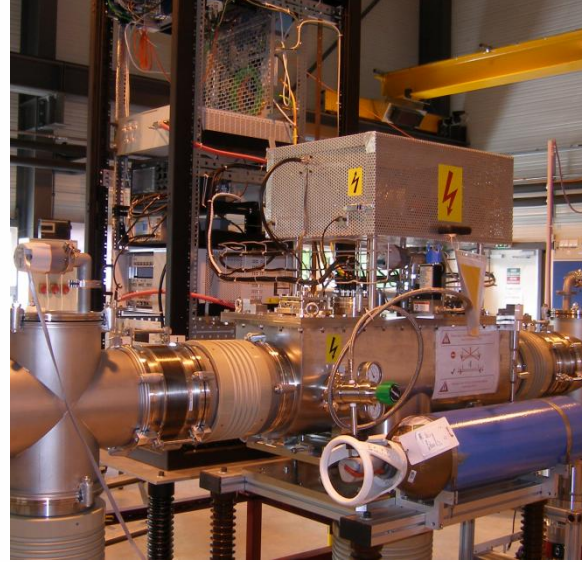




## Tunable LC BaLun Circuit



## GPIO-CB



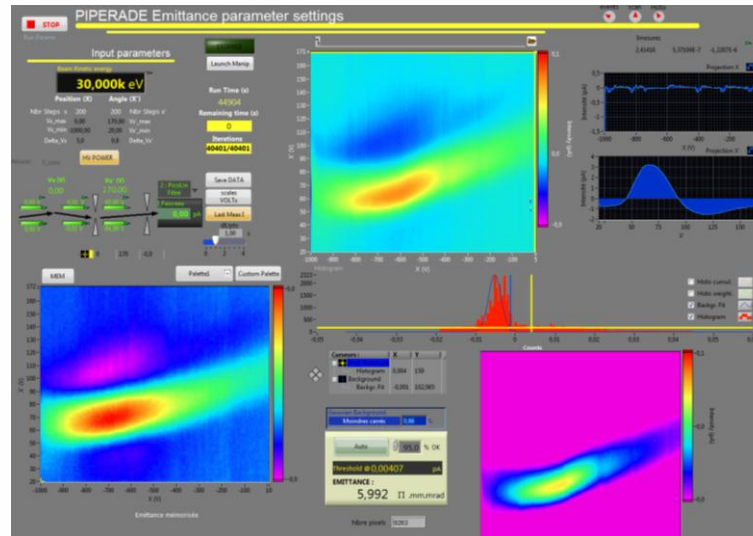
## PENNING Traps



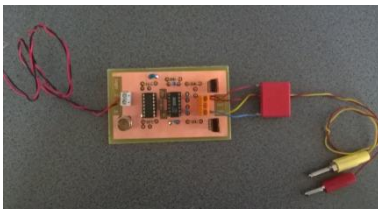
## FEBIAD ion source



## Emittance cooling



## Fast-Switches





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Thanks for your attention and to the PIPERADE collaboration



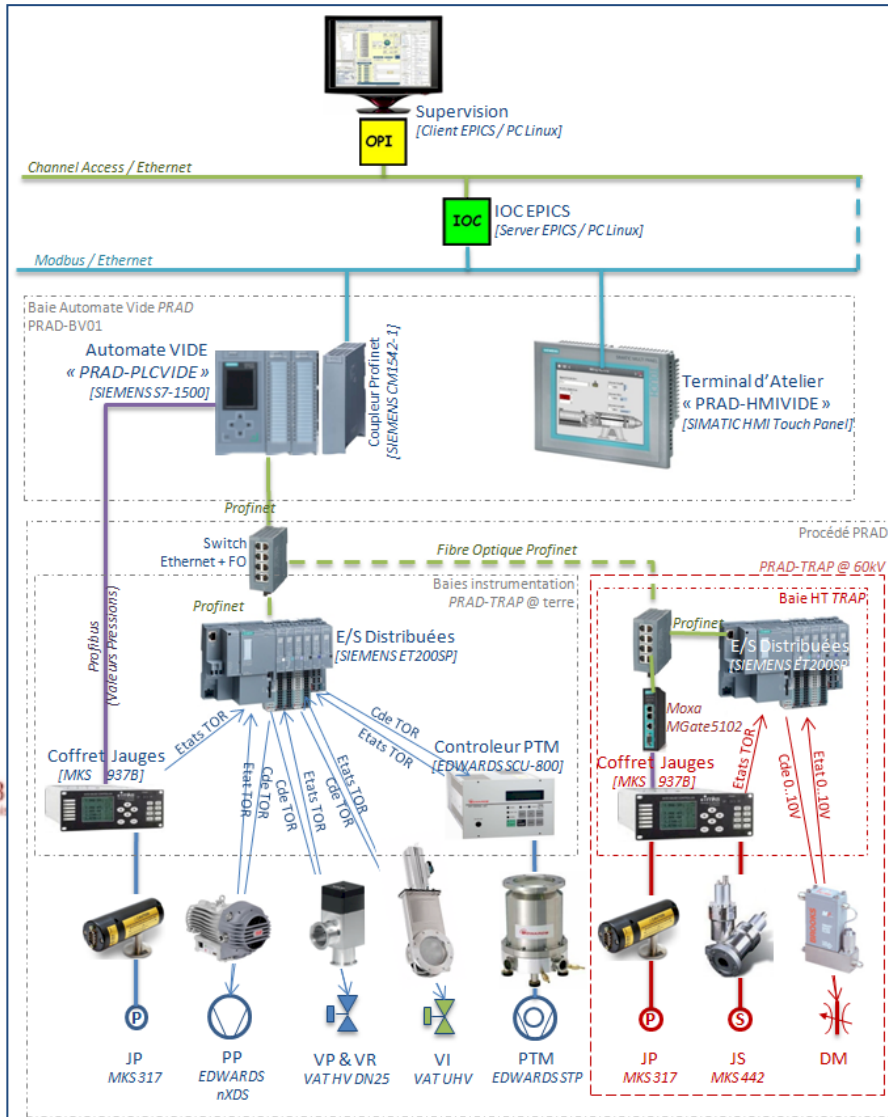
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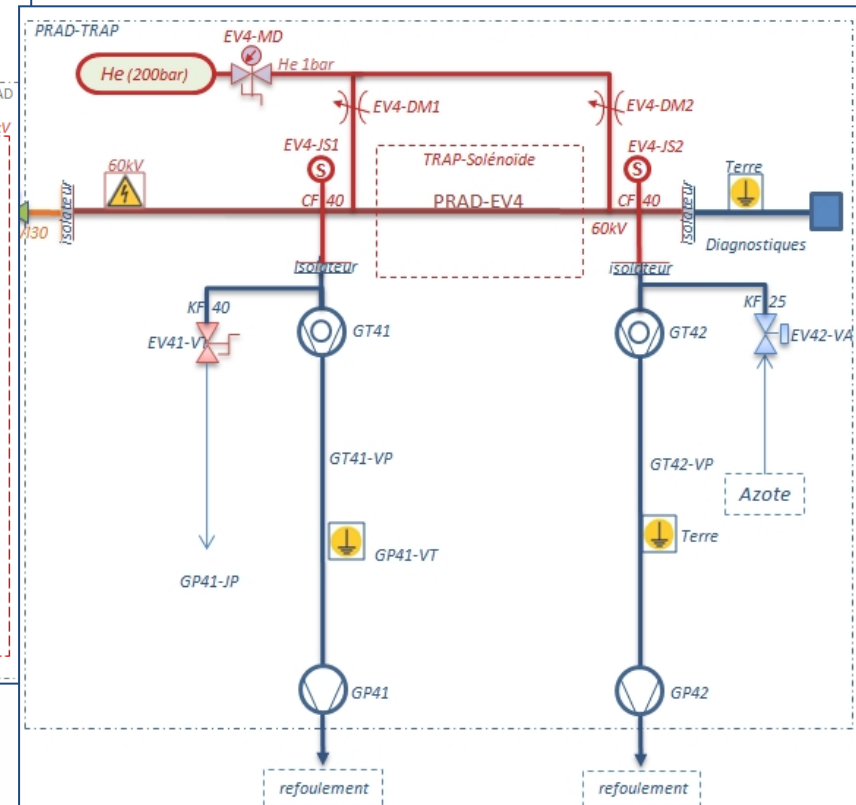
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- Siemens PLC
- Touch Panel for Local Control
- EPICS with Modbus-TCP
- Pumps at GND voltage
- Distributes I/O with Profinet (Fiber Optics if needed)
- Profibus for Gauges





# FT-ICR detection (Fourier Transform Ion Cyclotron Resonance)

