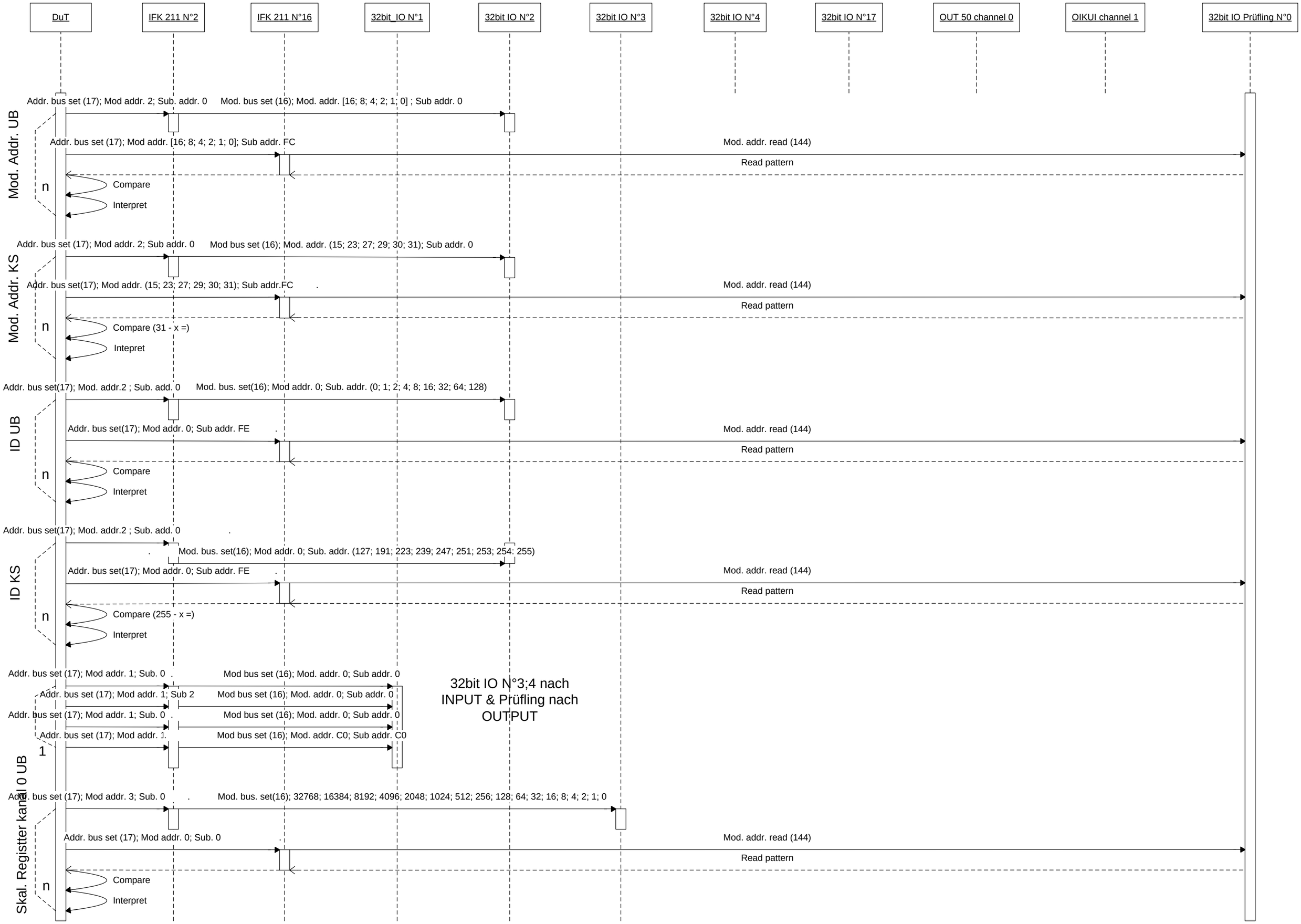
DuT
IFK 211 N°2
IFK 211 N°16
32bit_IO N°1
32bit IO N°2
32bit IO N°3
32bit IO N°4
32bit IO N°17
OUT 50 channel 0
OIKUI channel 1
32bit IO Prüfling N°0
Mod. Addr. UB
Addr. bus set (17); Mod addr. 2; Sub. addr. 0
Mod. bus set (16); Mod. addr. [16; 8; 4; 2; 1; 0] ; Sub addr. 0
Addr. bus set (17); Mod addr. [16; 8; 4; 2; 1; 0]; Sub addr. FC
Mod. addr. read (144)
Read pattern
n
Compare
Interpret
Mod. Addr. KS
Addr. bus set (17); Mod addr. 2; Sub addr. 0
Mod bus set (16); Mod. addr. (15; 23; 27; 29; 30; 31); Sub addr. 0
Addr. bus set(17); Mod addr. (15; 23; 27; 29; 30; 31); Sub addr.FC
Mod. addr. read (144)
Read pattern
n
Compare (31 - x =)
Intepret
ID UB
Addr. bus set(17); Mod. addr.2 ; Sub. add. 0
Mod. bus. set(16); Mod addr. 0; Sub. addr. (0; 1; 2; 4; 8; 16; 32; 64; 128)
Addr. bus set(17); Mod addr. 0; Sub addr. FE
Mod. addr. read (144)
Read pattern
n
Compare
Interpret
ID KS
Addr. bus set(17); Mod. addr.2 ; Sub. add. 0
Mod. bus. set(16); Mod addr. 0; Sub. addr. (127; 191; 223; 239; 247; 251; 253; 254; 255)
Addr. bus set(17); Mod addr. 0; Sub addr. FE
Mod. addr. read (144)
Read pattern
n
Compare (255 - x =)
Interpret
Skal. Registter kanal 0 UB
Addr. bus set (17); Mod addr. 1; Sub. 0
Mod bus set (16); Mod. addr. 0; Sub addr. 0
Addr. bus set (17); Mod addr. 1; Sub 2
Mod bus set (16); Mod. addr. 0; Sub addr. 0
Addr. bus set (17); Mod addr. 1; Sub. 0
Mod bus set (16); Mod. addr. 0; Sub addr. 0
Addr. bus set (17); Mod addr. 1
Mod bus set (16); Mod. addr. C0; Sub addr. C0
1
32bit IO N°3;4 nach
INPUT & Prüfling nach
OUTPUT
Addr. bus set (17); Mod addr. 3; Sub. 0
Mod. bus. set(16); 32768; 16384; 8192; 4096; 2048; 1024; 512; 256; 128; 64; 32; 16; 8; 4; 2; 1; 0
Addr. bus set (17); Mod addr. 0; Sub. 0
Mod. addr. read (144)
Read pattern
n
Compare
Interpret